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**Project: IEEE P802.15 Working Group for Wireless Personal Area Networks (WPANs)**

**Submission Title:** [Enhanced OQPSK Modulation with Orthogonal DSSS Sequences]

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**Source:** [Liang Li, Yafei Tian, Liang Zhang, Chenyang Yang, Zhijian Hu, ]

Company: [WXZJ Inc/Helicomm Inc]

Address: [Building D, No.2, Shangdi XinXi Lu, Beijing, China 100085 ]

Voice:[], E-Mail:[liang\_1@yahoo.com]

**Re:** [ IEEE 802.15.4 ]

**Abstract:** [This contribution proposes an updated PHY for TG4b.]

**Purpose:** [To encourage discussion.]

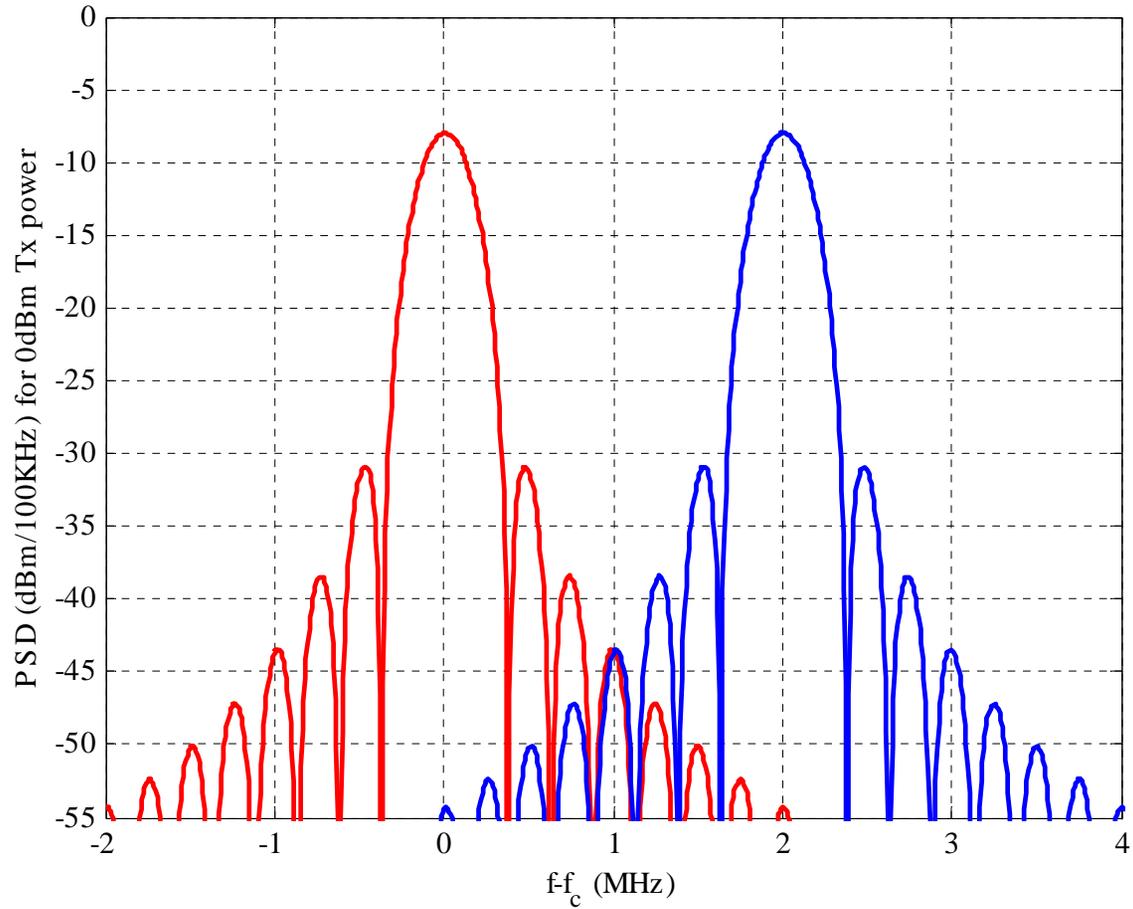
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# Proposal

- **A new PHY for low-rate WPAN that employs the MSK modulation scheme with new DSSS sequences:**
  - Keeps the present OQPSK modulation.
  - New DSSS sequences: 16 sequences for 4-bit mapping.
  - Data rate  $\geq 125\text{Kbps}$ .
  - Channel separation = 2MHz.
  - The 1<sup>st</sup> null-null bandwidth = 750KHz.

# Spectra



# Key Features of Our Proposal

- Improved DSSS sequences:
  - 16 DSSS sequences for 4-bit mapping.
  - Each sequence consists of 16 chips instead of 32, which results in  $\frac{1}{4}$  chip rate (500Kcps) for 125kbps data rate.
  - Better orthogonal characteristics between the modulated symbols and improving the decoding performance.

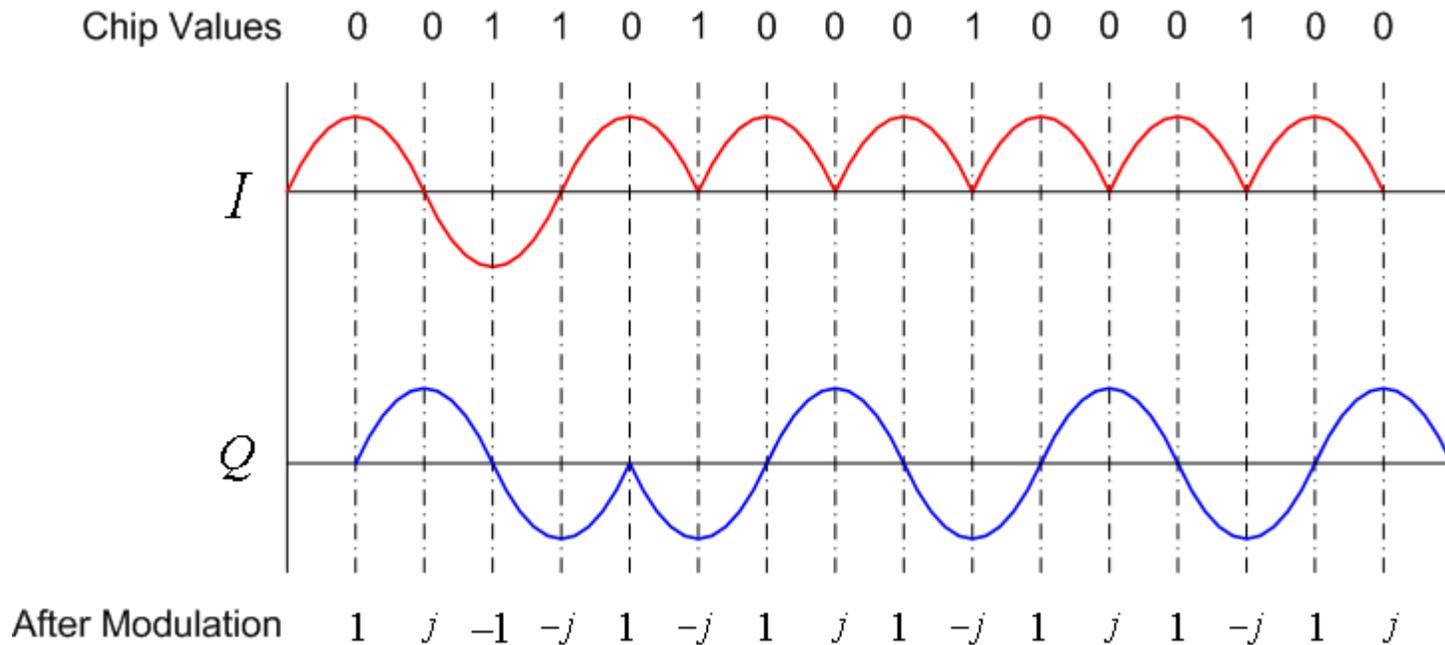
## Key Features of Our Proposal (cont.)

- Backward compatible:
  - The same modulation scheme  
OQPSK + Half-sine pulse shape
  - Keeps constant envelop and continuous phase.
- Lower RF requirements:
  - 2MHz channel separation, 750KHz main lobe
    - Corresponding to 5MHz channel separation and 3MHz main lobe of 15.4
  - Lower out-of-band emission.

# Generating Sequence

Decimal Symbol	Binary Symbol	Chip Values
0	0000	0011010001000100
1	1000	0110000100010001
2	0100	0000011101110111
3	1100	0101001000100010
4	0010	0011101101001011
5	1010	0110111000011110
6	1110	0000100001111000
7	0111	0101110100101101
8	0001	0011010010111011
9	1001	0110000111101110
10	0101	0000011110001000
11	1101	0101001011011101
12	0011	0011101110110100
13	1011	0110111011100001
14	0111	0000100010000111
15	1111	0101110111010010

# Modulation



The case for symbol 0

# Orthogonal Characteristic

With OQPSK modulation, half-sine pulse shaping and a sampling rate of 500KHz, after spreading and modulation, the symbol “0” can be written as:

$$\mathbf{s}_0 = [1 \quad j \quad -1 \quad -j \quad 1 \quad -j \quad 1 \quad j \quad 1 \quad -j \quad 1 \quad j \quad 1 \quad -j \quad 1 \quad j]^T$$

Similarly, we can get all 16 symbols  $\mathbf{s}_0, \mathbf{s}_1, \dots, \mathbf{s}_{15}$ , where

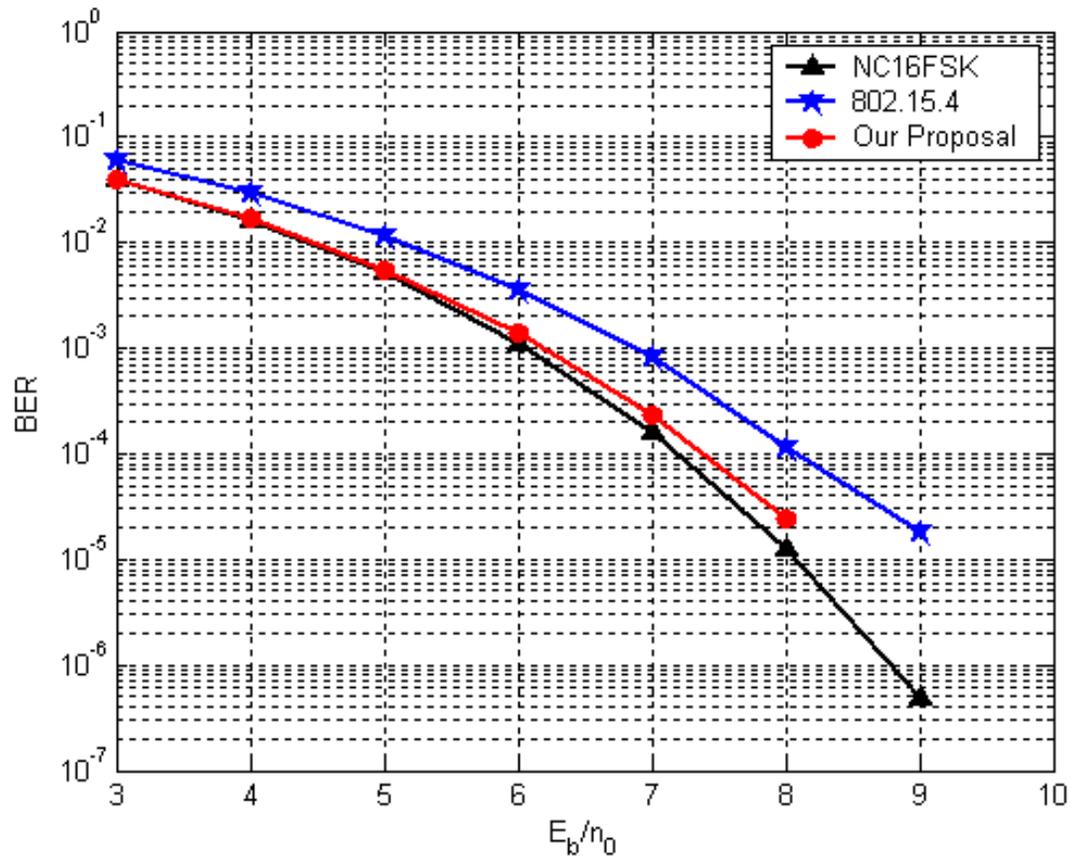
$$\mathbf{s}_m^H \mathbf{s}_n = \begin{cases} 16 & m = n \\ 0 & m \neq n \end{cases} \quad (m, n = 0, \dots, 15)$$

Sampled at the chip rate, the transmitted symbols keep their mutually orthogonal.

# Remarks

- Simulation shows the decoder having a 0.5 to 1dB improvement in BER vs.  $E_b/N_o$  performance over 802.15.4.
- Effective bandwidth of 750KHz for 125kbps
- Lower calculation complexity
- Lower requirements on RF filter
- Possible to implement a full-rate (250 kbps) system using 1M chip rate without adding any complexity and still conform to the spectrum specifications with a 2MHz channel bandwidth.

# Performance



NC-16FSK is the results of the non-coherent demodulation of 16-FSK which uses envelope detection.