IEEE P802.15  
Wireless Specialty Networks

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| IEEE 802.15.13  Text proposal for MAC general frame structure | | | | |
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Abstract

# This document contains a text proposal for MAC general frame structure.

1. **Overview**
2. **Normative references**
3. **Definitions, acronyms, and abbreviations**
4. **General description**
5. **MAC protocol specification**
   1. **MAC functional description**
   2. **General MAC frame format**

The MAC frame format is composed of a MHR, a MSDU, and a MFR. The fields of the MHR appear in a fixed order; however, the addressing fields may not be included in all frames. The general MAC frame shall be formatted as illustrated in Figure xx.

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
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|  | | | | |  |  |

|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Octet: 2** | **2** | **6** | **6** | **6** | **2** | **6** | **0/5/6/10**  **/14** | **variable** | **4** |
| Frame control | ACK information | Receiver Address | Transmitter Address | Destination Address | Sequence control | Source Address | Auxiliary security header | Frame payload | FCS |
| MHR | | | | | | | | MSDU | MFR |

* + 1. **Frame control field**

The frame control field is 2 octets in length and contains information defining the frame type, addressing  
fields, and other control flags. The frame control field shall be formatted as illustrated in Figure xx. Reserved bits are set to zero on transmission and ignored on reception.

|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
| **Bits: B0 B1** | **B2 B7** | **B8** | **B9** | **B10** | **B11** | **B12** | **B13 B15** |
| Frame version | Frame type / subtype | To DS | From DS | Security enabled | ACK request | Relay mode enable | Reserved |

* + - 1. **Frame Version subfield**

Text proposal (bit number; meaning of each bit/state)

The Frame Version subfield specifies the version number corresponding to the frame. This subfield shall be set to ‘00’ to indicate a frame compatible with IEEE P802.15.13. All other subfield values shall be reserved for future use.

* + - 1. **Frame type / subtype subfield**

Text proposal (bit number, meaning of each bit/state)

The Type field is 2 bits in length, and the Subtype field is 4 bits in length. The Type and Subtype fields together identify the function of the frame. There are three frame types in the current MAC layer protocol description: data, control and management. Each of the frame types has a number of defined subtypes. Table xx defines the valid combinations of type and subtype. (The numeric values in Table xx are shown in binary.)

|  |  |  |  |
| --- | --- | --- | --- |
| **Type value b3 b2** | **Type description** | **Subtype value b7 b6 b5 b4** | **Subtype description** |
| 00 | Management | 0000 | Association request |
| 00 | Management | 0001 | Association response |
| 00 | Management | 0010 | Reassociation request |
| 00 | Management | 0011 | Reassociation response |
| 00 | Management | 0100 | Probe request |
| 00 | Management | 0101 | Probe response |
| 00 | Management | 0110-0111 | Reserved |
| 00 | Management | 1000 | Beacon |
| 00 | Management | 1001 | Disassociation |
| 00 | Management | 1010 | Authentication |
| 00 | Management | 1011 | Deauthentication |
| 00 | Management | 1100-1111 | Reserved |
| 01 | Control | 0000 | Waveform Control |
| 01 | Control | 0001 | Advanced Modulation Control |
| 01 | Control | 0010 | CSI Control |
| 01 | Control | 0011 | High reliability control |
| 01 | Control | 0100-1111 | Reserved |
| 10 | Data | 0000 | Data |
| 10 | Data | 0001 | Null (no data) |
| 10 | Data | 0010-1111 | Reserved |
| 11 | Reserved | 0000-1111 | Reserved |

* + - 1. **To DS and From DS sub-fields**

The two subfields are needed to indicate if the transmission of frame involves Extended Service Set (ESS). ESS is the basic scenario required by TG13. The settings for the combinations of the two sub-fields for data frames are given in Table 1.

Table 1 To/From DS combinations in Data frames

|  |  |
| --- | --- |
| **To DS and From DS values** | **Meaning** |
| To DS = 0  From DS = 0 | A Data frame from one device to another device within the same IBSS or the same PBSS, a Data frame direct from one non-coordinator device to another non-coordinator device within the same infrastructure BSS, or a Data frame outside the context of a BSS.  This is the only valid combination for Data frames transmitted by an IBSS or PBSS device, or outside the context of a BSS. |
| To DS = 1  From DS = 0 | A Data frame destined for the DS or being sent by a device associated with a coordinator to the Port Access Entity in that coordinator. |
| To DS = 0  From DS = 1 | A Data frame exiting the DS or being sent by the Port Access Entity in a coordinator. |
| To DS = 1  From DS = 1 | Reserved |

For control frames, To DS and From DS, when present, are both zero.

For management frames, all combinations of the two sub-fields are reserved.

* + - 1. **Security enabled subfield**

The Security Enabled subfield is 1 bit in length, and it shall be set to one if the frame is protected by the MAC sublayer and shall be set to zero otherwise. The Auxiliary Security Header field of the MHR shall be present only if the Security Enabled subfield is set to one.

* + - 1. **ACK request subfield**

Text proposal (bit number, meaning of each bit/state)

The Acknowledgment Request subfield is 1 bit in length and specifies whether an acknowledgment is required from the recipient device on receipt of a data or MAC command frame. If this subfield is set to ‘1’, the recipient device shall send an acknowledgment frame. If this subfield is set to zero, the recipient device shall not send an acknowledgment frame.

* + - 1. **Relay mode enable**

This subfield relates to DA and SA in the general MAC format. It could be removed if DA and SA are permanently inserted in general MAC format. Otherwise, it is used to indicate if relay mode is relevant.

* + 1. **ACK information field**

|  |  |  |  |
| --- | --- | --- | --- |
| **Bits: B0 B4** | **B5 B14** | **B15** |  |
| Device address | Sequence number | ACK |  |

In all existing frame types and subtypes (except the Beacon frame), the ACK Info field contains the information necessary to identify the MSDU or MMPDU sequence number and the station which transmitted the acknowledged packet. The information in this field has the format depicted in Figure xx.

* + - 1. **Device address subfield**

Text proposal (bit number, meaning of each bit/state)

Bits 0 to 4 contain the address of the device which transmitted the packet. The device with such address is to be acknowledged by this ACK information field. In the uplink transmission, these bits identify the device transmitting the current packet as the acknowledgment can be only for packets transmitted by the coordinator.

* + - 1. **Sequence number subfield**

Text proposal (bit number, meaning of each bit/state)

Bits 5 to 14 identify the sequence number of the packet which is being acknowledged.

* + 1. **Addressing**

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **Octets:** | **6** |  | **6** | **6** | **6** |
| Destination OWPAN ID (optional) | Receiver address | Source OWPAN ID (optional) | Transmitter address | Destination address | Source address |

* + - 1. **Destination OWPAN ID subfield**

Text proposal (bit number, meaning of each bit/state)

* + - 1. **Destination address subfield**

Text proposal (bit number, meaning of each bit/state)

The Destination address subfield is a 48-bit field. It contains an IEEE MAC individual or group address that identifies the MAC entity or entities intended as the final recipient(s) of the MSDU (or fragment thereof) or A-MSDU, contained in the frame body field.

* + - 1. **Source OWPAN ID subfield**

Text proposal (bit number, meaning of each bit/state)

* + - 1. **Source address subfield**

Text proposal (bit number, meaning of each bit/state)

The Source address subfield is a 48-bit field. It contains an IEEE MAC individual address that identifies the MAC entity from which the transfer of the MSDU (or fragment thereof) or A-MSDU, contained in the frame body field was initiated.

* + - 1. **Receiver address subfield**

The Receiver Address field is a 48-bit field. It contains an IEEE MAC individual or group address that identifies the intended immediate recipient device(s) for the information contained in the frame body field.

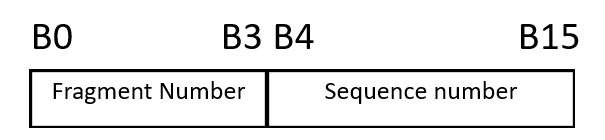
* + - 1. **Transmitter address subfield**

The Transmitter Address field is a 48-bit field. It contains an IEEE MAC address that identifies the device that has transmitted the MPDU contained in the frame body field.

* + 1. **Sequence control field**

Text proposal (bit number, meaning of each bit/state)

In data frames, the Sequence Control field contains the information necessary to identify the MSDU or MMPDU sequence number. The information in this field has the format depicted in Figure xx. Bits 4 to 15 identify the packet sequence number, while bits 0 to 3 are for packet fragmentation support.



* + 1. **Auxiliary Security Header field**

The Auxiliary Security Header field has a variable length and specifies information required for security processing, including how the frame is actually protected (security level) and which keying material from the MAC security PIB is used (see TBD). This field shall be present only if the Security Enabled subfield is set to one. For details on formatting, see TBD.

* + 1. **Frame Payload field**

Text proposal

The Frame Payload is a variable length field that contains information specific to individual frame types and subtypes as defined in subclause 5.2.1.2. The minimum frame body is 0 octets. The maximum length of the frame body is constrained or affected by the following:

* The maximum MMPDU, MSDU, A-MSDU, and MPDU sizes supported by the recipient(s) for the PPDU format in use, as specified in Table 1
* The fields present in the MAC header
* The presence of security encapsulation

Table 2 Maximum data unit sizes (in octets)

|  |  |  |  |
| --- | --- | --- | --- |
|  | PM PPDU | LB-OFDM PPDU | HB-OFDM PPDU |
| MMPDU size |  | 2304 |  |
| MSDU size |  | 2304 |  |
| A-MSDU size |  | PSDU size |  |
| MPDU size |  | No direct constraint on the maximum MPDU size; indirectly constrained by the maximum A-MSDU size, MSDU or MMPDU. |  |
| PSDU size |  |  |  |

* + 1. **FCS field**

Text proposal

The FCS field is a 32-bit field containing a 32-bit CRC. The FCS is calculated over all the fields of the MAC header and the Frame Body field. These are referred to as the calculation fields.

The FCS is calculated using the following standard generator polynomial of degree 32:

The FCS is the ones complement of the sum (modulo 2) of the following:

1. The remainder of ) divided (modulo 2) by , where is the number

of bits in the calculation fields, and

1. The remainder after multiplication of the contents (treated as a polynomial) of the calculation fields by and then divided by .

The FCS field is transmitted commencing with the coefficient of the highest-order term.

As a typical implementation, at the transmitter, the initial remainder of the division is pre-set to all ones and is then modified by division of the calculation fields by the generator polynomial . The ones complement of this remainder is transmitted, with the highest-order bit first, as the FCS field. At the receiver, the initial remainder is pre-set to all ones and the serial incoming bits of the calculation fields and FCS, when divided by , results in the absence of transmission errors, in a unique nonzero remainder value. The unique remainder value is the polynomial: