**IEEE P802.15**

**Wireless Personal Area Networks**

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| Project | IEEE P802.15 Working Group for Wireless Personal Area Networks (WPANs) | |
| Title | **D2 Comments Resolution Based PHY PIB Attributes Revision** | |
| Date Submitted | June, 2017 | |
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| Re: | PHY PIB attributes specification revision to Use of over-the-air PHY frame configuration is forbidden for PHY types IV, V and VI | |
| Abstract | Details of Resolutions regarding to the submitted Comments on D2 are suggested for PHY PIB Attributes Specification Revision to use of over-the-air PHY frame configuration is forbidden for PHY types IV, V and VI. The proposed method is designed to operate on the application services like LED ID using Color/QR Code, etc, LBS, Emergency EXIT Signage, LED-IT and Digital Signage with Advertisement Information etc. | |
| Purpose | D1 Comments Resolutions and Editorial Revision. | |
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**I. PHY TYPE IV PHY PIB Attributes**

# **1. PHY PIB Attributes for Offset-VPWM**

The PHY PIB attributes for Offset-VPWM is presented in the Table 179 —PHY PIB attributes (continued for Offset-VPWM).

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Attribute** | **Identifier** | **Type** | **Range** | **Description** |
| phySMFlashLIGHTApplicationSpecificMode | 0x10 | Unsigned | 0~255 | This attribute specifies the application specific PHY mode.  0 : Normal Data (Media Content, Information Content based on the Application used)  1 : ID Data  2 : Authentication Data |
| phyOffsetVPWMStdPERIOD | 0x11 | Integer | 0-65535 | This attribute specify the standard PWM period used to transmit the data (in micro secs) |
| phyOffsetVPWMOffsetPERIOD | 0x12 | Integer | 0-65535 | This attribute specify the Variable offset PWM period used to transmit the data (in micro secs) |

Table 179 — PHY PIB attributes (continued for OffsetVPWM)

**II. PHY TYPE VI PHY PIB Attributes**

# **1. PHY PIB Attributes for VTASC**

The PHY PIB attributes for VATSC is presented in the Table 179 —PHY PIB attributes (continued for VTASC).

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| --- | --- | --- | --- | --- |
| **PHY PIB Table 188 Additions** | | | | |
| **Attribute** | **Identifier** | **Type** | **Range** | **Description** |
| phyVTASCApplicationSpecificMode | 0x10 | Unsigned | 0~255 | This attribute specifies the application specific PHY mode.  0 : Normal Data (Media Content, Information Content based on the Application used for)  1 : LED ID Data  2 : Authentication Data |
| PhyVTASCFreq | 0x11 | Unsigned | 0~255 | This attribute specify the frame rate of VTASC sequence Transmission |
| phyVTASCCodeArea | 0x12 | Unsigned | 0~255 | This attribute specify the coded area of the IDE  0 : Full Screen  1 : Partial Screen  2~255 : Reserved |
| phyVTASCCodeLocation | 0x13 | Unsigned | 0~255 | This attribute specify the Coded Location of the VTASC  0 : Center  1 : Bottom Right  2 : Bottom Left  3 : Top Right  4 : Top Left  5~255 : Reserved |
| phyVTASCTLevel | 0x14 | Unsigned | 0~255 | This attribute specify the transparency Level of the VTASC |
| phyVTASCAHSize | 0x15 | Unsigned | 0~255 | This attribute specify the no of Horizontal Blocks in the VTASC |
| phyVTASCAVSize | 0x16 | Unsigned | 0~255 | This attribute specify the no of Vertical Blocks in the VTASC |
| phyVTASCSModel | 0x17 | Unsigned | 0~255 | This attribute specify the block shape Type used in the VTASC  0 : Square  1 : Circle  3 : hexagon  4 : star  5~65535 : Reserved |
| phyVTASCScalRateCtrl | 0x18 | Unsigned | 0~255 | This attribute specify the Scalable Rate control mode  0 : No Scalable Bitrate control  1 : Multirate Scalable Controller  2: Distance Adaptive Scalable Controller  3: Distance adaptive with multirate scalable controller |
| phyVTACScalRegion1OpticalClockRate | 0x19 | Unsigned | 0~255 | This attribute specify the scalable optical clock rate of VTASC region 1 |
| phyVTACScalRegion2OpticalClockRate | 0x1A | Unsigned | 0~255 | This attribute specify the scalable optical clock rate of VTASC region 2 |
| phyVTACScalRegion3OpticalClockRate | 0x1B | Unsigned | 0~255 | This attribute specify the scalable optical clock rate of VTASC region 3 |
| phyVTACScalRegion4OpticalClockRate | 0x1C | Unsigned | 0~255 | This attribute specify the scalable optical clock rate of VTASC region4 |
| PhyVTACSSCode1FP00 | 0x1D | Unsigned | 0~255 | This attribute specify the SS Code 1 pair code 0 |
| PhyVTACSSCode1FP01 | 0x1E | Unsigned | 0~255 | This attribute specify the SS Code 1 pair code 1 |
| PhyVTACSSCode2FP00 | 0x1F | Unsigned | 0~255 | This attribute specify the SS Code 2 pair code 0 |
| PhyVTACSSCode2FP01 | 0x20 | Unsigned | 0~255 | This attribute specify the SS Code 2 pair code 1 |
| PhyVTACSSCode3FP00 | 0x21 | Unsigned | 0~255 | This attribute specify the SS Code 3 pair code 0 |
| PhyVTACSSCode3FP01 | 0x22 | Unsigned | 0~255 | This attribute specify the SS Code 3 pair code 1 |
| PhyVTACSSCode4FP00 | 0x23 | Unsigned | 0~255 | This attribute specify the SS Code 4 pair code 0 |
| PhyVTACSSCode4FP01 | 0x24 | Unsigned | 0~255 | This attribute specify the SS Code 4 pair code 1 |
| phyVTASCCValue | 0x25 | Unsigned | 0~255 | This attribute specify the no of Colors used in the VTASC |
| phyVTASCTxHSize | 0x26 | Integer | 0-65535 | This attribute specify the no of Horizontal Pixel in the 2D Display Transmitter |
| phyVTASCTxVSize | 0x27 | Integer | 0-65535 | This attribute specify the no of Vertical Pixel in the 2D Display Transmitter |

Table 179 — PHY PIB attributes (continued for VTASC)

# **2. PHY PIB Attributes for SS2DC**

The PHY PIB attributes for SS2DC is presented in the Table 179 —PHY PIB attributes (continued for SS2DC).

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Attribute** | **Identifier** | **Type** | **Range** | **Description** |
| PhySS2DCApplicationSpecificMode | 0x10 | Unsigned | 0~255 | This attribute specifies the application specific PHY mode.  0 : Normal Data (Media Content, Information Content based on the Application used for)  1 : ID Data  2 : Authentication Data |
| PhySS2DCCodeArea | 0x11 | Unsigned | 0~255 | This attribute specify the coded area of the IDE  0 : Full Screen  1 : Partial Screen  2~255 : Reserved |
| PhySS2DCCodeLocation | 0x12 | Unsigned | 0~255 | This attribute specify the Coded Location of the SS2DC  0 : Center  1 : Bottom Right  2 : Bottom Left  3 : Top Right  4 : Top Left  5~255 : Reserved |
| phySS2DCTHSize | 0x13 | Unsigned | 0~255 | This attribute specify the no of horizontal blocks in the SS2DC |
| phySS2DCTVSize | 0x14 | Unsigned | 0~255 | This attribute specify the no of vertical blocks in the SS2DC |
| PhySS2DCCODEHSIZE | 0x15 | Unsigned | 0~255 | This attribute specify the horizontal size of the 2D code in the SS2DC |
| PhySS2DCCODEVSIZE | 0x16 | Unsigned | 0~255 | This attribute specify the vertical size of the 2D code in the SS2DC |
| phySS2DCTFrequency | 0x17 | Unsigned | 0~255 | This attribute specify the frame rate of SS2DC sequence Transmission |
| PhySS2DCTxHSize | 0x18 | Integer | 0-65535 | This attribute specify the no of Horizontal Pixel in the 2D Display Transmitter |
| PhySS2DCTxVSize | 0x19 | Integer | 0-65535 | This attribute specify the no of Vertical Pixel in the 2D Display Transmitter |

Table 179 — PHY PIB attributes (continued for SS2DC)

# **3. PHY PIB Attributes for IDE**

The PHY PIB attributes for IDE is presented in the Table 179 —PHY PIB attributes (continued for IDE).

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **PHY PIB Table 100 Additions** | | | | |
| **Attribute** | **Identifier** | **Type** | **Range** | **Description** |
| phyIDETxMode | 0x10 | Unsigned | 0-255 | This attribute indicates the Invisible Data Embedding transmission modes.  0 : IDE-BLENDING  1 : IDE-WATERMARK  2 : SS IDE-BLEND  3 : SS IDE-WATERMARK |
| phyIDEApplicationSpecificMode | 0x11 | Unsigned | 0~255 | This attribute specifies the application specific PHY mode.  0 : Normal Data (Media Content, Information Content based on the Application used for)  1 : ID Data  2 : Authentication Data  3~255: Reserved |
| phyIDEModulation | 0x12 | Unsigned | 0~255 | This attribute specifies the modulation.  0 : M-FSK  1 : HYBRID-MPFSK  2 : 2D Binary Code  3~255: Reserved |
| phyIDEFSKNoFrequency | 0x13 | Unsigned | 0~255 | This attribute specifies the number of frequency used in FSK |
| phyIDEPSKNoPhase | 0x14 | Unsigned | 0~255 | This attribute specifies the number of phase used in PSK |
| phyIDECodedArea | 0x15 | Unsigned | 0~255 | This attribute specify the coded area of the IDE  0 : Full Screen  1 : Partial Screen  2~255 : Reserved |
| phyIDECodedLocation | 0x16 | Unsigned | 0~255 | This attribute specify the Coded Location of the IDE  0 : Center  1 : Bottom Right  2 : Bottom Left  3 : Top Right  4 : Top Left  5~255 : Reserved |
| phyIDEHSize | 0x17 | Integer | 0-65535 | This attribute specify the no of horizontal pixel in the display |
| phyIDEVSize | 0x18 | Integer | 0-65535 | This attribute specify the no of vertical Pixel in the display |
| phyIDEENCHozAreaSize | 0x19 | Integer | 0-65535 | This attribute specify the no of horizontal pixel area to Encode |
| phyIDEENCVerAreaSize | 0x1A | Integer | 0-65535 | This attribute specify the no of horizontal pixel area to Encode |
| phyIDEMxNBlockSize | 0x1B | Unsigned | 0~255 | This attribute specify the no of Horizontal pixels in Blocks in the IDE  0 – 16x16 pixels  1 – 32x32 pixels  2 – 64x64 pixels  3~255: Reserved |
| phyIDEFrequency | 0x1C | Unsigned | 0~255 | This attribute specify the frame rate of IDE sequence Transmission |
| PhyIDETxHSize | 0x1D | Integer | 0-65535 | This attribute specify the no of Horizontal Pixel in the 2D Display Transmitter |
| PhyIDETxVSize | 0x1E | Integer | 0-65535 | This attribute specify the no of Vertical Pixel in the 2D Display Transmitter |

Table 179 - PHY PIB attributes (continued for IDE)