Project: IEEE P802.15 Working Group for Wireless Personal Area Networks (WPANs)

Abstract: [Evaluation of the MR-FSK channelization scheme and the effect of clock frequency tolerance with GFSK BT=0.5 as modulation format.]

Purpose: [Technical discussion. Presented to the 802.15.4g SUN Task Group for consideration.]

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Consideration of MR-FSK Channelization and Clock Frequency Tolerance Using GFSK

29th November 2010 *Khanh Tuan Le*

AGENDA

- Background
- Parameters of Interest
- Assessment Data
- Signal Spectrum
- Discussion

Background

- This contribution is submitted as a follow-up to the discussion of the DCN834rev1 at the November meeting in Dallas.
- Since the 802.15.4g MR-FSK PHY specifies the use of *filtered* frequency shift keying for good co-existence practice, the most realistically way to evaluate the channelization scheme and the effect of clock frequency tolerance is by using a filtered FSK signal.
- GFSK with BT=0.5 will be used as the 'filtered FSK' modulation format in this evaluation.

Parameters of Interest

Reference: P802.15.4g/D2

• The single sided clock frequency tolerance is defined as:

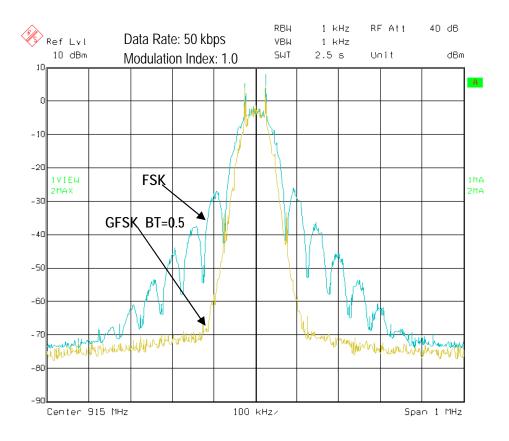
$$T \le \min\left(\frac{T_0 \times R \times h \times F_0}{R_0 \times h_0 \times F}, 50 ppm\right)$$

Max frequency tolerance: ±30 ppm for the 902-928 MHz band ±15 ppm for the 2400-2483.5 MHz band

- Adjacent channel rejection \geq 10 dB
- Alternate channel rejection \geq 30 dB

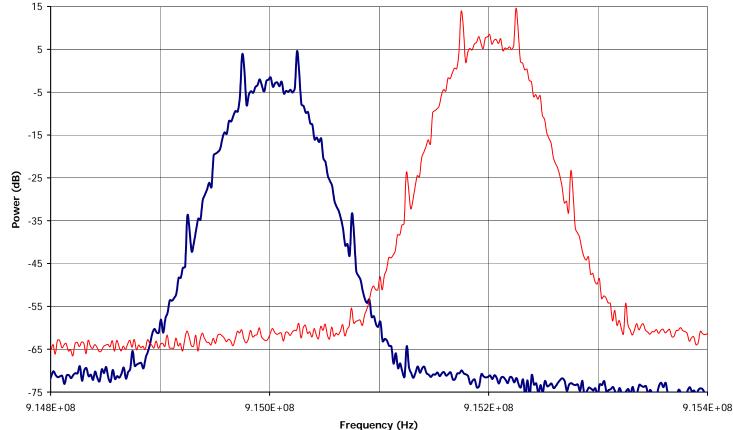
Assessment Data

- Data rate = 50 kbps
- Modulation index = 1.0
- Nominal channel spacing: 200 kHz
- GFSK with BT=0.5 is used as the filtered FSK modulation format because
- GFSK concentrates most of the signal power in the main lobe.
- GFSK has significantly lower interfering adjacent channel power leakge (ACPL) into neighboring channels.
- The interfering signal in the adjacent channel is a modulated signal with the same modulation characteristics as the wanted signal.
- The adjacent channel signal power is 10 dB higher than the wanted signal (according to P802.15.4g/D2).
- For worst case assessment the wanted and adjacent channel signals are offseted by the max frequency tolerance value specified in P802.15.4g/D2.



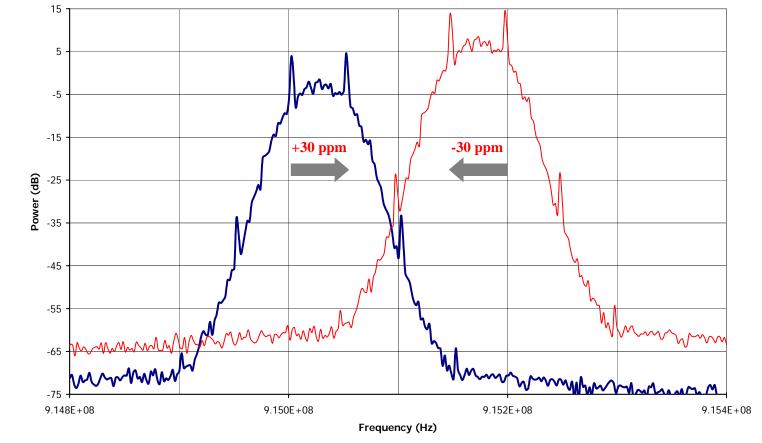
GFSK Signals at 915 MHz, ±0 ppm

- Wanted channel: 915.0 MHz
- Adjacent channel: 915.2 MHz
- Adjacent channel power
 relative to wanted channel:
 +10 dB



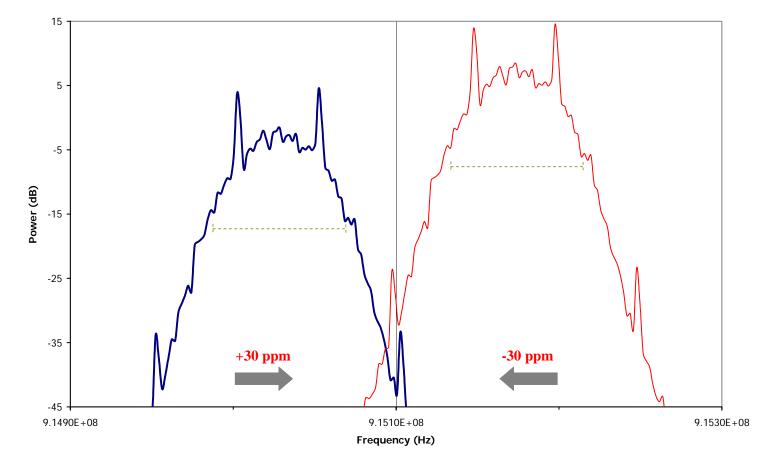
GFSK Signals at 915 MHz, ±30 ppm (1)

• *Negligible* interfering power leakage from adjacent channel at max frequency tolerance and worst case offset.



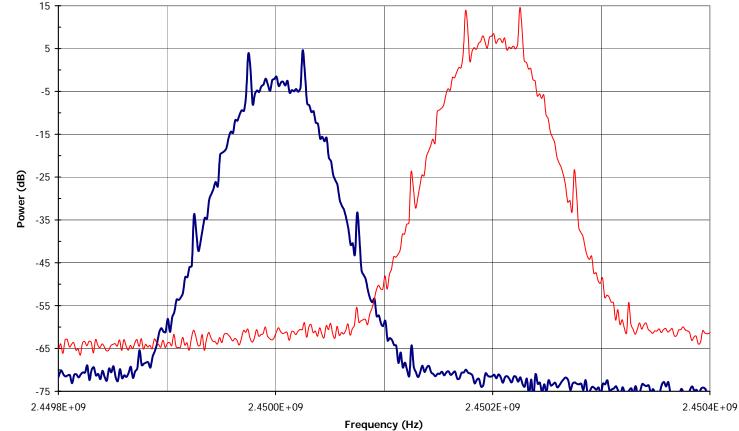
GFSK Signals at 915 MHz, ±30 ppm (2)

• Excellent conditions for signal demodulation at max frequency tolerance and worst case offset.



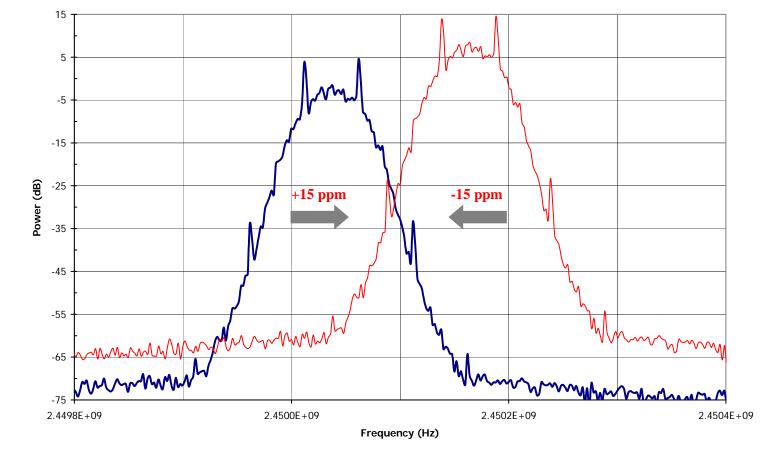
GFSK Signals at 2450 MHz, ±0 ppm

- Wanted channel: 2450.0 MHz
- Adjacent channel: 2450.2 MHz
- Adjacent channel power
 relative to wanted channel:
 +10 dB



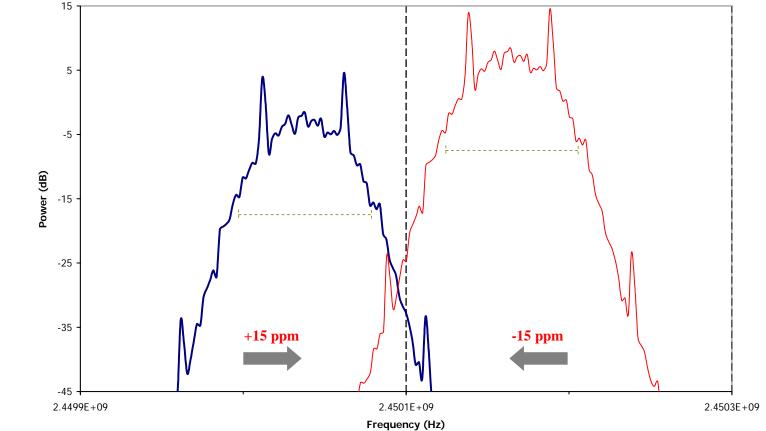
GFSK Signals at 2450 MHz, ±15 ppm (1)

 Negligible interfering power leakage from adjacent channel at max frequency tolerance and worst case offset.



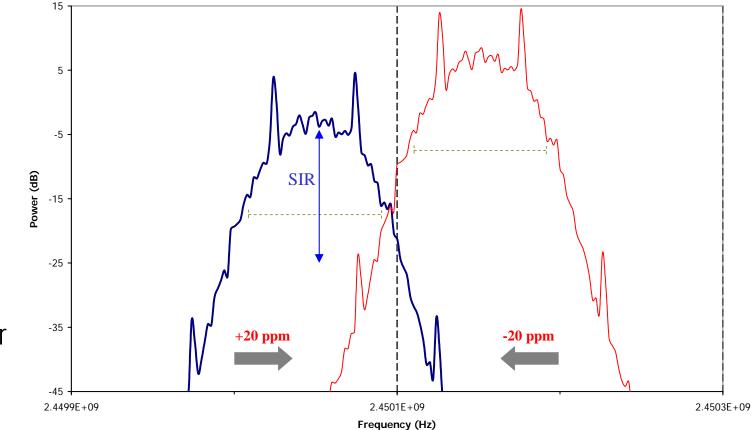
GFSK Signals at 2450 MHz, ±15 ppm (2)

- ±15 ppm (max frequency tolerance)
- Worst case frequency offset
- All signal power
 contained within the
 channel
- Good conditions for signal demodulation

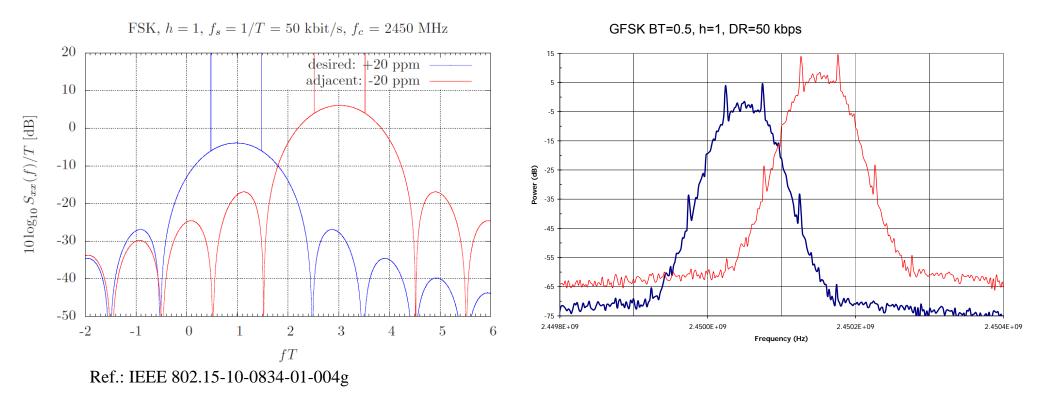


GFSK Signals at 2450 MHz, ±20 ppm

- ±20 ppm (33% higher than currently specified)
- Worst case frequency
 offset
- Most of signal power
 (>99%) still contained
 within the channel
- Adequate conditions for signal demodulation



GFSK and FSK Signals at 2450 MHz, ±20 ppm



• GFSK ('filtered FSK') with strong attenuation of the sidelobes provides a significant advantage also in situations with large frequency tolerances.

Discussion

- The adjacent and alternate channel test using 'an unmodulated carrier in the center of that channel' is most likely too optimistic. The recommendation is to change the interfering signal in the adjacent channel to a modulated signal in order to achieve more realistic evaluation of the receiver performance. The MR-O-QPSK and MR-OFDM PHY options use modulated interferer.
- The existing channel scheme and clock frequency tolerance work well at the 2400-2483.5 MHz band.
 A clock tolerance of ±20 ppm seems feasible at the 2.4 GHz band without further changes.
- The adjacent channel and alternate channel rejection requirements of respectively +10 dB and +30 dB (relatively simple to implement on low cost low power radios) are at a reasonable level for the 15.4g MR-FSK channelization scheme.

Thank you!