IEEE P802.11
Wireless LANs

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| LB271: Misc CIDs part 2 |
| Date: July 11, 2023 |
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 Abstract

This submission proposes resolutions for following 7 CIDs received for TGbe LB271:

15031, 15702, 17860, 17627, 16623, 16235, 17633

**Revisions:**

* Rev 0: Initial version of the document.

Interpretation of a Motion to Adopt

A motion to approve this submission means that the editing instructions and any changed or added material are actioned in the TGbe Draft. This introduction is not part of the adopted material.

***Editing instructions formatted like this are intended to be copied into the TGbe Draft (i.e. they are instructions to the 802.11 editor on how to merge the text with the baseline documents).***

***TGbe Editor: Editing instructions preceded by “TGbe Editor” are instructions to the TGbe editor to modify existing material in the TGbe draft. As a result of adopting the changes, the TGbe editor will execute the instructions rather than copy them to the TGbe Draft.***

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| **CID** | **Section** | **Pg.Ln** | **Comment** | **Proposed Change** | **Resolution** |
| 15031 | 9.4.2.312.2.3 | 257.35 | According to the description "The EMLMR Delay subfield indicates the minimum padding duration required for a non-AP MLD for EMLMR link switch when operating in EMLMR mode".It is proposed to change the name to "EMLMR Padding Delay" | As the comment | **Revised**The related comment was discussed and received majority support in CR document 11-23/366r8. **TGbe editor: please implement changes shown in document 11-23/366r8 tagged as 15925. No further changes in this document.** |
| 15702 | 9.4.2.312.2.3 | 256.08 | In EML capabilities subfield, EMLSR padding delay and EMLSR transition delay are both for EMLSR operation. But EMLMR operation only has an EMLMR delay which is a padding delay. Is the transition delay subfield necessary? | If transition delay is not necessary, please remove it for EMLSR operation; if it is necessary, please add it for EMLMR operation. | **Revised**The related comment was discussed and received majority support in CR document 11-23/366r8. **TGbe editor: please implement changes shown in document 11-23/366r8 tagged as 15925. No further changes in this document.** |
| 17860 | 9.4.2.312.2.3 | 257.35 | The EMLMR delay serves as both padding delay and transition delay for the EMLMR Mode. Change the name of the subfield to 'EMLMR padding and transition delay' and update the description. | As in comment | **Revised**The related comment was discussed and received majority support in CR document 11-23/366r8. **TGbe editor: please implement changes shown in document 11-23/366r8 tagged as 15925. No further changes in this document.** |
| 17627 | 9.3.3.2 | 198.25 | If the Reconfig ML element or T2LM element is present in the Beacon, add normative language that Beacon Frame Protection should be enabled by AP. Also, add normative language that a EHT non-AP should validate beacon before accepting a Reconfig ML element or T2LM element in the Beacon, and the EHT non-AP, upon receiving a Reconfig ML element or T2LM element in a Probe Reponse should attempt to receive and validate a beacon to confirm the information before accepting a Reconfig ML element or T2LM element. Ditto add normative language that a EHT non-AP STA should validate a Beacon before accepting CSA/ECSA. | As in comment | **Rejected**Beacon Protection is mandatory for an EHT AP to support (see 4.3.16a). Therefore, its support at the AP or STA does not depend on which elements are carried by the EHT AP.  |
| 16623 | 9.4.2.312.2.3 | 258.14 | Currently the maximum allowed value of the EMLSR Transition Timeout is 64 TUs or 640 ms. EMLSR is a dynamic mode and so it is expected that a non-AP would want to dynamically enable/disable EMLSR based on the traffic pattern, use case etc. A very long value of the Transition Timeout would make some of these use cases infeasible | Reduce the maximum value of the Transition Timeout to 16 TU or 32 TU | **Rejected**The allowed values for EMLSR Transition Timeout were discussed in the last round (LB266) and the TGbe group agreed with the values defined in D3.2. See CR document [https://mentor.ieee.org/802.11/dcn/22/11-22-2175-02-00be-proposed-resolutions-to-lb266-cids-on-emlsr-entering-and-exit-process.docx] for related discussions.  |
| 16235 | 9.4.1.75 | 219.54 | There should be more than 15 link available within an MLD. This appears to be an artificial value and there is room for many more links both within an MLD and in Figure 9-144I | Change the clause title to "Link ID field".Delete the initial sentence in clause 9.4.1.75.Delete the Figure 9-144I.Change the final sentence of the clause to:"The Link ID subfield indicates the identifier of the link, which is described in the element carrying the Link ID Info field (see 35.3.3.2 (Link ID))." | **Rejected**The comment fails to identify a technical issue that needs to be resolved. The Link ID Info field is used in various contexts where a separate (sub)field is required to signal the Link ID value which includes 4 bits. As a result, the remaining 4 bits are reserved. |
| 17633 | 9.4.2.312.2.3 | 256.22 | Padding field has a duration (which depends on PHY data rate of the PPDU it is sent in) BUT that is not what is meant here I'm sure. | Try "the minimum MAC padding duration \*signaled by\* the Padding field ..." | **Revised**Agree with the commenter. The suggested change is made.**TGbe editor: please implement the changes shown in document 11-23/1162r0 tagged as 17633.** |

**Discussion**

None

**Changes**

***TGbe editor: Please note Baseline is 11be D3.2***

**9.4.2.312.2 Basic Multi-Link element**

**9.4.2.312.2.3 Common Info field of the Basic Multi-Link element**

***TGbe editor: please update the following paragraphs as shown below [CID 17633]***

The EMLSR Padding Delay subfield indicates the minimum MAC padding duration signaled by (#17633) the Padding field of the initial Control frame requested by the non-AP MLD as defined in 35.3.17 (Enhanced multi-link single radio operation). When the EMLSR Padding Delay subfield is included in a frame sent by an AP affiliated with an AP MLD, the EMLSR Padding Delay subfield is reserved. The EMLSR Padding Delay subfield includes 3 bits and is set as defined in Table 9-401e (Encoding of the EMLSR Padding Delay subfield).