IEEE P802.11
Wireless LANs

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| LB266 CR for 36.2.2 RU\_ALLOCATION |
| Date: 2022.07.12 |
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Abstract

This submission contains the proposed comment resolutions of the following 5 CIDs in 22/0971 IEEE 802.11be LB266 comments, for the parameter RU\_ALLOCATION in the subclause 36.2.2 TXVECTOR and RXVECTOR parameters.

CIDs 11337, 11338,

Revision Notes

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| R0 | Initial revision |

## CID 11337

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| Page.Line | Clause Number | Comment | Proposed Change | Resolution |
| 556.26 | 36.2.2 | After "in the EHT variant User Info field", add " in trigger frame format" | as in the comment | REVISED.***Instructions to the editor:*** **Please make the changes as shown under CID 11337 in 11-22/1076r1.** |

***Instructions to the editor: please make the following changes to Line 26, Page 556 in the subclause 36.2.2 TXVECTOR and RXVECTOR parameters in D2.0 as shown below (RU\_ALLCATION: FORMAT is EHT\_MU and EHT\_PPDU\_TYPE is equal to 0):***

For the RXVECTOR, 9 bits are used to indicate the RU or MRU allocated to the user in the whole band using the same encoding of PS160 (B39) and RU Allocation (B12–B19) subfields in the EHT variant User Info field of a Trigger frame.

## CID 11338

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| Page.Line | Clause Number | Comment | Proposed Change | Resolution |
| 556.36 | 36.2.2 | Clarify what are those 9 bits. Suggest using the same language as in the EHT\_MU case like 9 bits encoding of PS160(B39) and RU Allocation (B12-B19( subfilelds in the EHT ....". Make the same change to P562 L61 | as in the comment | REVISED.The proposed descprition is more accurate than the original one.***Instructions to the editor:*** **Please make the changes as shown under CID 11338 in 11-22/1076r1.** |

***Instructions to the editor: please make the following changes to Line 36, Page 556 in the subclause 36.2.2 TXVECTOR and RXVECTOR parameters in D2.0 as shown below (RU\_ALLOCATION: FORMAT is EHT\_TB):***

9 bits are used to indicate the RU or MRU allocated to the user in the whole band using the same encoding of PS160 (B39) and RU Allocation (B12–B19) subfields in the EHT variant User Info field of a Trigger frame.

***Instructions to the editor: please make the following changes to Line 61, Page 561 in the subclause 36.2.3 TRIGVECTOR parameters in D2.0 as shown below (RU\_ALLOCATION\_LIST):***

9 bits are used per STA to indicate the RU allocated in the whole bandwidth using the same encoding of PS160 (B39) and RU Allocation (B12–B19) subfields in the EHT variant User Info field of a Trigger frame. See the RU Allocation subfield description in 9.3.1.22.4 (EHT variant User Info field) for more information of each entry.

## CID 12180 & 12864 & 12865

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| --- | --- | --- | --- | --- |
| Page.Line | Clause Number | Comment | Proposed Change | Resolution |
| 556.18(CID 12180) | 36.2.2 | "144 bits for a 320 MHz-1 or 320 MHz-2 PPDU", the number of "RU\_ALLOCATION" bits does not distinguish between 320 MHz-1 and 320 MHz-2. There also no definition and usage of "320 MHz-1 or 320 MHz-2 PPDU" elsewhere. | change to "144 bits for a 320 MHz PPDU" | ACCEPTED.Note: The resolutions of CIDs 12180, 12864, and 12865 are the same. |
| 556.18(CID 12864) | 36.2.2 | Define "320 MHz-1 or 320 MHz-2 PPDU". |  | REVISED.No need to distinguish 320 MHz-1 and 320 MHz-2 PPDUs here. It is fine to use 320 MHz PPDU.***Instructions to the editor:*** Change “320 MHz-1 or 320 MHz-2 PPDU” to “320 MHz PPDU”.Note: The resolutions of CIDs 12180, 12864, and 12865 are the same. |
| 556.18(CID 12865) | 36.2.2 | The number of "RU\_ALLOCATION" bits does not cover 320 MHz-1 and 320 MHz-2. | change"144 bits for a 320 MHz-1 or 320 MHz-2 PPDU" to "144 bits for a 320 MHz PPDU" | ACCEPTED.Note: The resolutions of CIDs 12180, 12864, and 12865 are the same. |

**Discussion:**

9 bits for a 20 MHz PPDU;
18 bits for a 40 MHz PPDU;
36 bits for a 80 MHz PPDU;
72 bits for a 160 MHz PPDU;
144 bits for a 320 MHz PPDU.

Agree with the commenter. No need to distinguish 320 MHz-1 and 320 MHz-2 PPDUs here. It is fine to use 320 MHz PPDU.

**Discussion ends.**