IEEE P802.11
Wireless LANs

|  |
| --- |
| Proposed Draft Text (PDT-PHY): Frequency Tolerance |
| Date: 2020-08-06 |
| Author(s): |
| Name | Affiliation | Address | Phone | email |
| Myeongjin Kim | Samsung |  |  | mj1108.kim@samsung.com |
| Wook Bong Lee | Samsung |  |  | wookbong.lee@samsung.com |

Abstract

This submission proposed the draft text on frequency tolerance for TGbe D0.1.

This document is based on 27.3.19.3 Transmit center frequency and symbol clock frequency tolerance of P802.11ax D6.1.

This draft is only for 20/40/80/160/320MHz transmission. Didn’t include 80+80/160+160MHz yet as there is some related discussion.

Yellow highlighted texts are TBD.

Revision 1: feedback during conference call

Revision 2: add 80+80/160+160 MHz with TBD (highlighted)

xx.3.19.3 Transmit center frequency and symbol clock frequency

Transmit center frequency and the symbol clock frequency for all transmit antennas and frequency segments shall be derived from the same reference oscillator. The symbol clock frequency and transmit center frequency tolerance shall be ±20 ppm in the 5 GHz and 6 GHz bands and ±25 ppm in the 2.4 GHz band. EHT TB PPDU format is subject to additional requirements as defined in xx.3.15 (Transmit requirements for PPDUs sent in response to a triggering frame).

Transmit signals with TXVECTOR parameter CH\_BANDWIDTH set to CBW160 or CBW80+80 may be generated using two separate RF LOs, one for each of the lower and upper 80 MHz frequency portions.

NOTE—The signal phase of the two 80 MHz frequency portions might not be correlated.

Transmit signals with TXVECTOR parameter CH\_BANDWIDTH set to CBW320 or CBW160+160 may be generated using two separate RF LOs, one for each of the lower and upper 160 MHz frequency portions.

NOTE—The signal phase of the two 160 MHz frequency portions might not be correlated.