IEEE P802.11
Wireless LANs

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| Resolution of CIDs 3573, 3583, 3592, 3638, 3657, 3689 |
| Date: 2018-11-12 |
| Author(s): |
| Name | Affiliation | Address | Phone | email |
| Solomon Trainin | Qualcomm |  | 972547885738 | strainin@qti.qualcomm.com |
| Assaf Kasher | Qualcomm |  |  | akasher@qti.qualcomm.com  |
| Alecsander Eitan | Qualcomm |  |  | eitana@qti.qualcomm.com  |
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Resolution of CID 3573, 3583, 3592, 3638, 3657, 3689

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| **CID** | **Page** | **Line** | **Clause** | **Comment** | **Proposed Change** | **Resolution** |
| 3573 |  |  | 9.4.2.23 | Quiet element can be present in more frames than listed here | Change the first sentence in the last paragraph in the section to "The Quiet element is optionally present in Beacon frames, as described in 9.3.3.3, DMG Beacon frames, as described in 9.3.4.2, Announce frames, as dscribed in 9.6.22.2, and Probe Response frames, as described in 9.3.3.11"I prefer not to refer to spec section for each frame when mentioong the frame name (people can just do a quick search for the frame format), but staying consistent with baseline text. And I don't think all sections defining an element have followed this convention. | **Reject**The DMG AP does not support the Quiet element.As defined in **11.8.3**“A non-VHT AP shall not transmit a Quiet Channel element.” |
| 3583 |  |  |  | Continuation of Block Ack policy across TDD slots | A-MPDUs sent in one or more TDD slots before a reverse-direction TDD slot need ot be treated logically as a single burst of MPDUs resulting in a single Block Ack frame.Consider allowing norml Ack policy (implicit Block Ack) for all A-MPDUs in one-direction TDD slots, and allowing BAR frames in data only slots. With BAR frames, consider allowing multiple BAR frames in subsequent TDDslots without generating multiple Block Ack frames. | **Reject**1. Ack policy = Block Ack enables transmsssion of sequence of multiple A-MPDUs. 2. The conditions of making the decision what Ack policy to use in the TDD SP are not different from those in SP and TXOP.3. The existent solution allows to achieve the goal. 4. Sending BAR in the Data slot is allowed. See 10.40.6.2.2 SP with TDD channel access (IEEE P802.11ay/D2.1, October 2018) |
| 3592 |  |  |  | To combat interference, synchronized TDD devices need to be able to abort receiving alien packets that happen to cross the TDD slot boundaries (one way to tell if they are alien). | Add an RX-ABORT primitive tto PHY SAP | **Revised**See proposal in this document |
| 3638 | 166.00 | 7 | 9.6.21.2 | There are few TDD IE in the Announce frame format that are optionally present, but the conditions are not specified | Specify the conditions when the elements are presented | **Revised**See proposal in this document |
| 3657 | 171.00 | 4 | 9.7.3 | There is contradiction between definition of A-MPDU contents and TDD requirement of Data type TDD slot that does not allow transmission of other frames | Resolve the contradiction by definition of A-MPDU content for TDD slot | **Revised**See proposal in this document |
| 3689 | 68.00 | 1 | 9.2.4.7.1 | IEEE 802.11-2016 holds Table 9-19--Maximum data unit sizes (in octets) and durations (in microseconds) which needs some changes for EDMG. | Please add Table 9-19 with modified EDMG data unit sizes and lengths. | **Revised**See proposal in this document |

***CID 3592***

***TGay editor append new subclaue after 8.3.5.16***

**8.3.5.17 PHY-ABORT.request**

**8.3.5.17.1 Function**

This primitive is a request by the MAC to reset the PHY. The PHY is always reset to the receive state

**8.3.5.17.2 Semantics of the service primitive**

This primitive has no parameters.

**8.3.5.17.3 When generated**

This primitive is generated at any time to reset the PHY.

**8.3.5.17.4 Effect of receipt**

Receipt of this primitive by the PHY causes the PHY entity to reset both the transmit and the receive state

machines and places the PHY into the receive state.

***CID3638***

**9.6.21.2 Announce frame format**

***TGay editor change Table 9-458 as indicated below***

|  |  |  |
| --- | --- | --- |
| **Order**  | **Information**  | **Notes**  |
| 23  | TDD Slot Structure  | This element is optionally present if the TDD Channel Access Supported subfield is set to 1 in the DMG Capabilities element transmitted by the STA; otherwise not present. If present, the element specifies the TDD slot timing. |
| 24  | TDD Slot Schedule  | This element is optionally present if the TDD Channel Access Supported subfield is set to 1 in the DMG Capabilities element transmitted by the STA; otherwise not present. If present, the element specifies the type of the TDD slot and the access permission. |
| 25  | TDD Route  | This element is optionally present if the TDD Channel Access Supported subfield is set to 1 in the DMG Capabilities element transmitted by the STA; otherwise not present. If present, the element specifies theTDD beamforming results and sector switch configuration. |
|  |  |  |
| 27  | TDD Bandwidth Request  | This element is optionally present if the TDD Channel Access Supported subfield is set to 1 in the DMG Capabilities element transmitted by the STA; otherwise not present. If present, the element specifies the parameters needed for bandwidth reservation during TDD SPs.  |
| 28  | TDD Synchronization  | This element is optionally present if the TDD Channel Access Supported subfield is set to 1 in the DMG Capabilities element transmitted by the STA; otherwise not present. If present, the element specifies the information needed for clock synchronization during TDD SPs.  |

***CID 3657***

**9.7.3 A-MPDU contents**

***TGay editor add NOTE 3 to the notes below Table 9-527—A-MPDU contexts***

NOTE 3— If a STA transmits an A-MPDU in the TDD slot, the A-MPDU is constrained so that the content of the A-MPDU is compliant wih the TDD slot category as defined in 10.40.6.2.2 SP with TDD channel access.

***CID 3689***

***TGay editor add column to the Table 9-25—Maximum data unit sizes (in octets) and durations (in microseconds) as follows***

**Table 9-25—Maximum data unit sizes (in octets) and durations (in microseconds)**

|  |  |  |
| --- | --- | --- |
|  |  | **EDMG PPDU** |
| MMPDU size |  | 2304 |
| MSDU size |  | 7920 without SAR agreement, 4,194,303 under SAR agreement  |
| A-MSDU size |  | 7935 |
| MPDU size |  | See Note 5 |
| PSDU size(see NOTE 7) |  | 222-1(see Table 53, and Table 60) |
| PPDU duration(see NOTE 7) |  | 2000(see Table 20-29 (DMG PHYcharacteristics)) |

**References:**

1. IEEE P802.11ay/D2.1, October 2018
2. IEEE P802.11-REVmd/D2.0, December 2018