IEEE P802.11  
Wireless LANs

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| Comment resolution | | | | |
| Date: 2018-03-05 | | | | |
| Author(s): | | | | |
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Resolution of CIDs 1069, 1072, 1081, 1083, 1112, 1163, 1164, 1188, 1189, 1217, 1254, 1255, 1256, 1257, 1258, 1259, 1260, 1285, 1380, 1381, 1656, 1692, 1722, 1858, 1859, 1867, 1940, 1943, 2113, 2114, 2173, 2174, 2201, 2202, 2203, 2400 is presented

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| **CID** | **Page** | **Clause** | **Comment** | **Proposed Change** |
| 1069 |  | 10.6.2.1 | It is not clear whether the MAC level fragmentation is allowed in combination with the EDMG segmentation. From the frame formats, it is likely to be not allowed. | Clarify whether the MAC level fragmentation is allowed in combination with the EDMG segmentation. |

**Proposal: Revised**

Discussion:

There is no subclause 10.6.2.1 as in the comment and no page number is presented. I expect that the comment is related to 10.62.1 of 10.62 (EDMG segmentation and reassembly operation). The suggested clarification already exists in the subclause 10.62.1 “A pair of STAs that use segmentation and reassembly for a particular TID shall not employ MSDU fragmentation for this TID.” Suggest modifying to add reference.

***TGay Editor: modify (Draft 1.1)***

A pair of STAs that use segmentation and reassembly for a particular TID shall not employ MSDU fragmentation defined in 10.5 for this TID.

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| **CID** | **Page** | **Clause** | **Comment** | **Proposed Change** |
| 1072 | 34.17 | 9.3.1.9.7 | "... A-MPDUs with a length that is not less than indicated by Maximum A-MPDU Length Exponent (Table 3)" Can A-MPDUs exceed Maximum A-MPDU Length? | Change to "... A-MPDUs with a length equal to the length indicated by Maximum A-MPDU Length Exponent (Table 3)" |

**Proposal: Revised**

Discussion:

Proposed text is better than existing.

***TGay Editor: modify in* Table 1 — RBUFCAP encoding for the EDMG Compressed BlockAck variant *P53 (Draft 1.1) as follows:***

Indicates that the recipient’s memory has enough space to receive A-MPDUs with a length equal to the length indicated in Maximum A-MPDU Length Exponent (Table 5)

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| **CID** | **Page** | **Clause** | **Comment** | **Proposed Change** |
| 1081 | 112.23 | 10.13.7 | 802.11ah introduced "S-MPDU" as a general term. So, there is no need to add a new terminology for EDMG case. | Use S-MPDU for the whole case. |
| 2400 | 35.00 | 9.3.1.9.8 | S-MPDU is introduced in 11ah, is defined in REVmd\_D 0.5. 11ax also replaces single MPDU to S-MPDU | Change "single MPDU" to "S-MPDU". Do it through the whole draft. |

**Proposal: Accept**

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| **CID** | **Page** | **Clause** | **Comment** | **Proposed Change** |
| 1083 | 196.12 | 10.62.1 | It looks like the MPDU sequence number is the same with the original sequence number. The relation between the original sequence number should be explained somewhere in section 9. | As in comment. |

**Proposal: Reject**

Discussion:

Sequence Control field that covers the relevant sequence number is defined in 9.2.4.4.1 (Sequence Control field)

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| **CID** | **Page** | **Clause** | **Comment** | **Proposed Change** |
| 1112 | 31.07 | 9.2.4.4.1 | The Start of MSDUn subfield is set to one to indicate that the MPDU contains the first segment of an MSDU. It is set to zero otherwise. | Either change the logic to "The Start of MSDUn subfield is set to zero to indicate..." or remove the n from the subfield name |

**Proposal: Reject**

Discussion:

The comment and the proposal are unclear, and the suggested change does not have any technical impact.

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| **CID** | **Page** | **Clause** | **Comment** | **Proposed Change** |
| 1163 | 196.19 | 10.62.2 | "until it is successfully delivered to the recipient STA" -- this cannot be guaranteed | append "or the entire MSDU is discarded" |

**Proposal: Accept**

It is about P236L21 in the Draft 1.1

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| **CID** | **Page** | **Clause** | **Comment** | **Proposed Change** |
| 1164 | 196.16 |  | Segmentation looks like the existing fragmentation mechanism. | Either explain what segmentation offers that fragmentation does not, or remove it entirely.  If kept, it might help readers understand which mechanism to use by adding an introductory para explaining when it is appropriate to use the mechanism. |
| 1722 | 195.29 | 10.62 | The Segmentation feature appears to be an enhancement to the existing (very similar) Fragmentation feature. The enhancement is the negotiation of the size of the 'segments', using ADDTS. This enhnacement could be added to the existing Fragmentation, without creating a whole new, parallel, and nearly identical facility. This would also help with numerous small technical errors around wording like "partitioning an MSDU into a sequence of MPDUs." | Remove the new EDMG segmentation facility, and add the negotiation of maximum 'segment/fragment' size as a negotiated extension of the Fragmentation facility. |

**Proposal: Revised**

Discussion: There are few issues in the current spec that SAR resolves.

* MPDU size is limited by max A-MSDU size of 7935 octets that the size is limited by existent FCS of 4 octets.
* The fragmentation is not supported by HT Immediate BlockAck

SAR keeps max MPDU size of 7935 octets while max MSDU is no more limited by this size. The MSDU size negotioated over the SAR establishment. SAR is also an extension of HT Immediate BlockAck. There are substantial changes that are not related to the existent Fragmentation definition.

***TGay Editor: modify in P235L37 (Draft 1.1)***

The segmentation and reassembly mechanism allows a STA to receive at the MAC SAP a MSDU of a size that is optimal for upper layers and application and is not limited to the maximum transmission unit (MTU) The MSDU is delivered to the MAC SAP of the destination STA through MSDU segments carried within MPDUs transmitted over the wireless link using HT Immediate BlockAck mechanism. The max MSDU size is negotiated between communicating peers over the SAR establishment.

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| **CID** | **Page** | **Clause** | **Comment** | **Proposed Change** |
| 1188 | 35.05 | 9.3.1.9.8 | A two bit field should be enumerated as integers when its values are defined, for stylistic similarity to the baseline, and to avoid ambiguity of endianness. | Flip a coin and replace AckType subfield values 00, 10, 11, 01 with 0,1,3,2 or 0,2,3,1.  Order by increasing values.  Consider not introducing a reserved value in the middle of the enumeration. |

**Proposal: Revised**

Discussion: Implement as per comment

***TGay Editor: modify Table 2 —AckType subfield definition (Draft 1.1) as follows***

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| **AckType subfield value** | **TID subfield value** | **Presence of Block Ack Starting Sequence Control subfield and Block Ack Bitmap subfields** | **Context of a Per TID Info subfield in a Multi-STA BlockAck frame** |
| 0 | 0-15 | Present | Block acknowledgment context:  Sent as a response to MPDUs in an A-MPDU that solicit an immediate block acknowledgement or to a BlockAckReq frame. |
| 1 | 0-15 | Not present | Acknowledgment context:  Sent as a response to an MPDU or EDMG single MPDU that solicits an immediate acknowledgment. |
| 2 | 0-15 | Not present | All-ack context:  Sent as a response to an A-MPDU that solicits an immediate response and all MPDUs contained in the A-MPDU are received successfully. |
| 3 | N/A | N/A | Reserved |

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| **CID** | **Page** | **Clause** | **Comment** | **Proposed Change** |
| 1189 | 35.08 | 9.3.1.9.8 | "The bitmap length is equal to 2(3+ BlockAck Bitmap Subfield Length)."    Units should be provided because the name is poorly chosen. Normally one would expect the units of "bitmap length" to be bits. | Add "octets" before the period. |

**Proposal: Accept**

***TGay Editor: modify in P54L8 (Draft 1.1)***

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| **CID** | **Page** | **Clause** | **Comment** | **Proposed Change** |
| 1217 | 119.25 |  | "EDMG Multi-TID BlockAck variant and Multi-TID BlockAckReq variant" -- what is this supposed to mean? Both frames are valid? I think this text assumes that "variant" refers to the encoding of the variant, and not the format of the frame itself - which is wrong. | If this is not supposed to allow the non-EDMG Multi-TID BlockAckReq, delete "and Multi-TID BlockAckReq variant""    Same possible issue in the previous line with the compressed variant. |

**Proposal: Reject**

Discussion:

I think that the commenter didn’t pay attention that the sentence is about BlockAck variant and BlockAckReq variant so there is no contradiction at all. There is no EDMG Multi-TID BlockAckReq variant defined and the existent Multi-TID BlockAckReq variant is in use. Both mentioned frames are valid for EDMG STA

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| **CID** | **Page** | **Clause** | **Comment** | **Proposed Change** |
| 1254 | 30.15 | 9.2.4.4.1 | There is no figure to illustrate of the sequence control field (when segmentation and reassebmly is not supported) | Include the figure for sequence control field (when segmentation and reassebmly is not supported) |

**Proposal: Reject**

Discussion:

As already defined in the text “If segmentation and reassembly is not supported, the Sequence Control field consists of two subfields, the Sequence Number and the Fragment Number, and its format is illustrated in Figure 9-4. If segmentation and reassembly is supported, the Sequence Control field has the format illustrated in Figure 4. See the Figure 9-4 in the basic spec.

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| **CID** | **Page** | **Clause** | **Comment** | **Proposed Change** |
| 1255 | 30.04 | 9.2.4.4.1 | The bit position of the MSDU Sequence Number field in Figure 4 should be B(MSDU Modulo+2) if the starting bit position of this field is B2 | Modify the diagram with correct bit position |
| 1256 | 30.04 | 9.2.4.4.1 | The starting bit position of the MPDU Sequence Number field in Figure 4 should be B(MPDU Modulo+3). | Modify the diagram with correct bit position |
| 1257 | 30.04 | 9.2.4.4.1 | the length of the MPDU Sequence Number field in Figure 4 is illustrated as B(MSDU Modulo+2), should it be B(MPDU Modulo+3) | Correct the diagram |

**Proposal: Reject**

Discussion:

Let’s take an example of MSDU Modulo = 5. In this case the MSDU SN field is [B2 – B6], it is 5 bits allocated for MSDU Modulo. The MPDU SN field is [B7-B15] – it is 9 bits allocated for MPDU SN. 9+5 = 14 equal to the MSDU modulo + MPDU modulo. So, no issues to fix are found.

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| **CID** | **Page** | **Clause** | **Comment** | **Proposed Change** |
| 1285 | 31.07 | 9.2.4.4.1 | "The Start of MSDUn subfield is set to one to indicate that......"  Change "set to one to " to " set to 1 to" for keeping the same style as REVmd | As in comment.  Much places need to change, if this comment is approved. |

**Proposal: Reject**

Discussion:

Propose keeping on the editor’s responsibility to resolve the style issues.

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| **CID** | **Page** | **Clause** | **Comment** | **Proposed Change** |
| 1380 | 31.04 | 9.2.4.4.1 | It is not clear what is the structure of this field (reserved bits), when MSDU Modulo+MPDU Modulo<14 | Add reserved bits |
| 1381 | 32.14 | 9.3.1.8.3 | It is not clear what is the structure of this field (reserved bits), when MSDU Modulo+MPDU Modulo<14 | Add reserved bits |

**Proposal: Revised**

Discussion: The commented issue is resolved per CID 1735 (IEEE 802.11-18/0278r2) that defined “The sum of the values of the MSDU Modulo subfield and the MPDU Modulo subfield shall be equal to fourteen.”

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| **CID** | **Page** | **Clause** | **Comment** | **Proposed Change** |
| 1656 | 197.30 | 10.63 | Limiting to TIDs that correspond to the AC that gains the access essentially make Multi-TID A-MPDU the 2-TID A-MPDU for SU (without considering TSIDs), which are very constrained. It is reasonable to allow the 11ax-style Multi-TID aggregation rule which is to allow any higher AC MPDU as long as the TXOP boundary is maintained. | Please revise |

**Proposal: Revised**

Discussion:

It is already resolved as proposed by the commenter in resolution to CID’s 1956, 2272 (IEEE 802.11-18/0137r2)

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| **CID** | **Page** | **Clause** | **Comment** | **Proposed Change** |
| 1692 | 196.08 | 10.62.1 | "of different MSDUs" cannot be because is the segmentation of a single MSDU | replace "different MSDUs" with "different MPDUs" |

**Proposal: Reject**

Discussion:

It is allowed to transmit multiple MSDUs and the reordering buffer may contain multiple MSDU’s. There is no place in the definition that limits the flow to single MSDU. Different MSDU’s are identified by MSDU SN.

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| **CID** | **Page** | **Clause** | **Comment** | **Proposed Change** |
| 1858 | 31.13 | 9.2.4.4.1 | As described in 10.24.2 the block Ack agreement is between the originating STA and the intended receiving STA. It should be noted in the text. | Change "the recipient's" to "the intended recipient's" |
| 1859 | 31.17 | 9.2.4.4.1 | As described in 10.24.2 the block Ack agreement is between the originating STA and the intended receiving STA. It should be noted in the text. | Change "the recipient's" to "the intended recipient's" |

**Proposal: Revised**

***TGay Editor: modify as in the Proposed Change in P49L13, P49L17, P51L19, and P51L23 (Draft 1.1)***

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| **CID** | **Page** | **Line** | **Clause** | **Comment** | **Proposed Change** |
| 1867 | 99.24 | 24 | 9.71 | The A-MPDU format for call rules on EOF padding subframes. VHT PPDUs and PPDUs set the maximum length of the A-MPDU based on pre-EOF padding. The maximum length of an A-MPDU in an EDMG PPDU should be based on the same criteria. | Change "A-MPDU in an EDMG PPDU" to "A-MPDU pre-EOF padding in an EDMG PPDU" |

**Proposal: Reject**

Discussion:

1. The PPDU length is the same for all users
2. Each user gets a length field in header-B covering the length of the actual data of the PSDU (Table-45)
3. Per each user, there is PHY padding to the maximum length of all users (30.5.9.4.4 MU PPDU padding and space-time streams mapping
4. From a MAC perspective, there is no need for any EOF padding to compensate for MU PPDU size, all is done at the PHY level.
5. Max A-MPDU length is per user (indicated by Maximum A-MPDU Length Exponent). A-MPDU length includes EOF padding, so the RX buffer can accommodate entire A-MPDU of maximal length. Header B contains PSDU length that is equal to the A-MPDU length.

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| **CID** | **Page** | **Clause** | **Comment** | **Proposed Change** |
| 1940 | 86.01 | 9.4.2.268 | Actual transmission or receiving in a TDD slot the STA is assigned to depends on the STA role. Existing language is that AP STA transmits in TX Slot and non-AP STA transmits in RX Slot. The existing definition requires separate TX/RX rules for AP and non-AP STA that misleads implementation of the lower MAC. Propose to clarify definition of the TDD slot to unify the behavioral rule. | Modify definition of TDD slots that instead of TX TDD and RX TDD make it clear that each slot is TX for one STA and RX slot for another STA thus unify definition in 10.36.6.2.2 to avoid double rules covered AP STA and non-AP STA. Submission is ready to present |

**Proposal: Revised**

Discussion:

Definition in 9.4.2.268 and behavioral rules in 10.36.6.2.2 in the IEEE P802.11ay/D1.1, February 2018 are modified following the Proposed Change, still the 10.3.2.10 Acknowledgement procedure is not aligned with the new definition of 9.4.2.268. The modification below resolves the issue

***TGay Editor: modify as follows (Draft 1.1)***

**10.3.2.10 Acknowledgement procedure**

P132L38

A STA operating in an SP with TDD channel access (see 10.37.6.2.2), upon reception of a frame that requires acknowledgement shall transmit an Ack or BlockAck frame at the start of the earliest occurring TDD slot the STA is assigned to, with Bitmap and Access Type Schedule field (Table 17)

of the TDD slot set to TX, and with slot category of the TDD slot set to Basic TDD slot, as indicated in the TDD Slot Schedule element (see 9.4.2.268).

P133L6

A STA operating in an SP with TDD channel access (see 10.37.6.2.2) has AckTimeout interval value equal to the duration from the PHY-TXEND.confirm primitive of the current frame to the end of the earliest occurring TDD slot the addressed recipient of the MPDU is assigned to, with Bitmap and Access Type Schedule field (Table 17) of the TDD slot set to RX, and with slot category of the TDD slot set to Basic TDD slot, as indicated in the TDD Slot Schedule element (see 9.4.2.268).

***TGay Editor remove next paragraph***

**10.25.7.5 Generation and transmission of BlockAck frames by an HT STA or DMG STA**

P150L7

A STA operating in an SP with TDD channel access (see 10.37.6.2.2), that receives a PPDU that contains a BlockAckReq frame in which the Address 1 field matches its MAC address during either full-state or partial-state operation shall transmit a PPDU containing a BlockAck frame starting at the earliest occurring TDD slot the STA is assigned to, with Bitmap and Access Type Schedule field (Table 17) of the TDD slot set to TX, and with slot category of the TDD slot set to Basic TDD slot, as indicated in the TDD Slot Schedule element.

***TGay Editor remove next paragraph***

P150L19

A STA operating in an SP with TDD channel access (see 10.37.6.2.2), that receives an A-MPDU that contains one or more MPDUs in which the Address 1 field matches its MAC address with the Ack Policy field equal to Normal Ack (i.e., implicit block ack request) during either full-state operation or partial-state operation shall transmit a PPDU containing a BlockAck frame starting at the earliest occurring TDD slot the STA is assigned to, with Bitmap and Access Type Schedule field (Table 17) of the TDD slot set to TX, and with slot category of the TDD slot set to Basic TDD slot, as indicated in the TDD Slot Schedule element.

***TGay Editor remove next paragraph***

**10.25.7.7 Originator’s behaviour**

P154L10

A DMG STA originator operating in an SP with TDD channel access (see 10.37.6.2.2) shall not transmit more than one MPDU or A-MPDU that has an Ack Policy of Normal Ack to another STA per each occurrence of a TDD slot the STA is assigned to, with Bitmap and Access Type Schedule field (Table 17) of the TDD slot set to TX, and with slot category of the TDD slot set to Basic TDD slot, as indicated in the TDD Slot Schedule element.

***TGay Editor remove next paragraph***

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| **CID** | **Page** | **Clause** | **Comment** | **Proposed Change** |
| 1943 | 28.01 | 8.3.5.12.2 | Terms of Primary Channel Offset, secondary, secondary1, and secondary2 are illustrated in Figure 1 and Figure 2 but are not defined. | Provide definition of the terms. Definition is provided as a table in separate submission |

**Proposal: Revised**

Discussion:

The CID is resolved in IEEE 802.11-18/0377r2

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| **CID** | **Page** | **Clause** | **Comment** | **Proposed Change** |
| 2113 | 30.12 | 9.2.4.4.1 | "The Sequence Control field is 16 bits in length and its format depends whether segmentation and reassembly (see 10.62) is supported between the transmitting and receiving STA. If segmentation and reassembly is not supported, the Sequence Control field consists of two subfields, the Sequence Number and the Fragment Number, and its. The format of the Sequence Control field is illustrated in Figure 9-4. If segmentation and reassembly is supported, the Sequence Control field has the format illustrated in Figure 4. The Sequence Control field is not present in Control frame" The The Sequence Control field is not stricly dependent on whether seg and reassmebly is supported or not. It should be rephrased to say the The Sequence Control field formta in case of segmenataion and reassmebly is supported | as suggested |

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| 2202 | 30.12 | 12 | 9.2.4.4.1 | It seems unnecessary to complicate the Sequence control field structure with the changes necessary for segmentation and reassembly. Add a clause for Sequence Control field structure for segmentation and reassembly. This would simplify the specification and not complicate the legacy text. | Do not added these changes to 9.2.4.4.1, create a new clause 9.2.4.4.x for the segmentation and reassembly case. |

**CID’s 2113 2202**

**Proposal: Revised**

Discussion:

Agree to add new subclause as follows:

***TGay Editor: modify as follows (Draft 1.1)***

P48L11

The Sequence Control field is 16 bits in length and consists of two subfields, the Sequence Number and the Fragment Number, and it is illustrated in Figure 9-4. The Sequence Control field is not present in Control frames.

*Insert at P49L4*

**9.2.4.4.x Sequence Control field in SAR**

If SAR is supported as defined in 10.62, the Sequence Control filed consists of four subfields Start of MSDUn, End of MSDUn, MSDU Sequence Number, and MPDU Sequence Number if segmentation and reassembly is supported (10.62)., The format is illustrated in Figure 4.

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| **CID** | **Page** | **Clause** | **Comment** | **Proposed Change** |
| 2114 | 30.12 | 9.2.4.4.1 | Is Segmentation and Reassmebly feature applied to HT, VHT, HE? The frame format here does not indicate this is applied to EDMG only but the reference is made to EDMG section | clarify |

**Proposal: Reject**

Discussion:

Provided reference to 10.62 completely resolves the commented issue. Conditions to support SAR are defined there that the format is part of it. The optional feature as defined is EDMG specific and capability to support it is provided in the EDMG capabilities that are EDMG specific and not applicable to other amendments.

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| **CID** | **Page** | **Clause** | **Comment** | **Proposed Change** |
| 2173 | 30.16 | 9.2.4.4.1 | "Figure 4" is not the correct format | Change "Figure 4" to the correct format |
| 2174 | 31.05 | 9.2.4.4.1 | "Figure 4" is not the correct format | Change "Figure 4" to the correct format |

**Proposal: Reject**

Discussion:

It is editor’s decision to keep global enumeration of new added figures in the draft, it is not specific to the Figure 4. Should be resolved of the entire draft at the time decided by the editor

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| **CID** | **Page** | **Clause** | **Comment** | **Proposed Change** |
| 2201 | 32.06 | 9.3.2.8.3 | It seems unnecessary to complicate the Compressed BlockAckReq variant with the changes necessary for segmentation and reassembly. Added a clause for Compressed BlockAckReq variant for segmentation and reassembly, would simplify the specification and not complicate the legacy text. | Do not added these changes to 9.3.1.8.3, create a new clause 9.3.1.8.x for the segmentation and reassembly case. |

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| 1258 | 32.04 | 9.3.1.8.3 | The bit position of the MPDU Starting Sequence Number filed in Figure 5 -Bar Information field format should be B(MSDU Modulo+2) if the starting bit position of this field is B2 | Modify the diagram with correct bit position |
| 1259 | 32.04 | 9.3.1.8.3 | The starting bit position of t MPDU Starting Sequence Number filed in Figure 5 -Bar Information field formatshould be B(MPDU Modulo+3). | Modify the diagram with correct bit position |
| 1260 | 32.04 | 9.3.1.8.3 | the length of the MPDU Starting Sequence Number filed in Figure 5 -Bar Information field format is illustrated as B(MSDU Modulo+2), should it be B(MPDU Modulo+3) | Correct the diagram |

**Proposal: Revised**

Discussion:

*There is no need to change the BA Starting Sequence Control field format of the Compressed BlockAckReq variant. The existent BA Starting Sequence Control field of the Compressed BlockAckReq variant is fine to be used to shift MSDU under SAR. The SAR format modifications should be removed.*

***TGay Editor: modify as follows (Draft 1.1)***

P51L6

*Remove sentence:* In case of a block ack agreement that does not use segmentation and reassembly (see 10.62),

P51L13

*Remove text that starts with* “In case of a block ack agreement that uses segmentation and reassembly…” and ends by … element contained in the recipient’s ADDBA Response frame (see 10.25.2).”

*Remove* **Figure 5 —BAR Information field format**

P143L40

Remove:

“and the size of the MSDU Starting Sequence Number and MPDU Starting Sequence Number subfields of the BAR Information field (see Figure 5)”

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| **CID** | **Page** | **Clause** | **Comment** | **Proposed Change** |
| 2203 | 93.10 | 9.6.5.2 | It seems unnecessary to complicate the ADDBA Request frame format with the changes necessary for segmentation and reassembly. Add a clause for the ADDBA request frame format for segmentation and reassembly. This would simplify the specification and not complicate the legacy text. | Do not added these changes to 9.6.5.2, create a new clause 9.6.5.x for the segmentation and reassembly case. |

**Proposal: Revised**

Discussion:

*There is no need to change the BA Starting Sequence Control field format of the ADDBA Request frame. The existent BA Starting Sequence Control field of the ADDBA Request frame is fine to be used to set the MSDU SN under SAR. Reference to the Figure 5 should be removed.*

***TGay Editor: modify as follows (Draft 1.1)***

P143L38

***TGay Editor remove:*** “which serves to confirm the size of the MPDU Sequence Number and MSDU Sequence Number subfields in the Sequence Control field and the size of the MSDU Starting Sequence Number and MPDU Starting Sequence Number subfields of the BAR Information field (see Figure 5).”

P120L6

***TGay Editor modify:***

The Starting Sequence Number subfield of the Block Ack Starting Sequence Control field (see Figure 9-27) contains the sequence number of the first or next (in the case of a renegotiation of a block ack agreement) MSDU to be sent under this block ack agreement. The Fragment Number subfield is set to 0.

**References:**

1. IEEE P802.11ay/D1.1
2. IEEE Std 802.11-2016