IEEE P802.11
Wireless LANs

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| Comment resolutions for 10.22.2.4 |
| Date: 2018-01-05 |
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Abstract

This submission proposes resolutions for multiple comments related to TGax D2.0 with the following CIDs:

* 11154, 12445 (2 CIDs)

Revisions:

* Rev 0: Initial version of the document.

Interpretation of a Motion to Adopt

A motion to approve this submission means that the editing instructions and any changed or added material are actioned in the TGax Draft. This introduction is not part of the adopted material.

***Editing instructions formatted like this are intended to be copied into the TGax Draft (i.e. they are instructions to the 802.11 editor on how to merge the text with the baseline documents).***

***TGax Editor: Editing instructions preceded by “TGax Editor” are instructions to the TGax editor to modify existing material in the TGax draft. As a result of adopting the changes, the TGax editor will execute the instructions rather than copy them to the TGax Draft.***

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| **CID** | **Commenter** | **P.L** | **Comment** | **Proposed Change** | **Resolution** |
| 11154 | Adrian Stephens | 195.25 | "During an EDCA TXOP, the Address 2 field excluding the Individual/Group bit of all control frames sent by a TXOP holder shall be setto the same address value."Prior to this statement, a TXOP holder might use part of the TXOP to talk to one STA, and part to talk to another. It can use RTS to start an exchange a SIFS after the receipt of an Ack from the first STA to start the exchange with a second STA. There might be good reason to do this.The new statement makes previously compliant behaviour non-compliant. | Limit the addition to HE STAs. | Revised –The statement restricts the setting of the transmitter’s addess (TA) so that the same value of TXOP holder is maintained during the TXOP. The reason that this restriction was added in 11ax is because a multi-BSS AP might have been able to serve as part of different BSSs its associated STAs. The requirement here was added to not allow such a case. In previous amendments this would not have been the case. As such the rule is backwards compatible as well. The commenter is invited to identify what behavior of a legacy STA would have caused to be noncompliant due to the addition of this statement. But, as usual, don’t mess with legacy so incorporating the change anyways. TGax editor to make the changes shown in 11-18/0009r0 under all headings that include CID 11154. |
| 12445 | Liwen Chu | 195.25 | Change to "During an EDCA TXOP, the Address 2 field excluding the Individual/Group bit of all control frames in an SU PPDU sent bya TXOP holder shall be set to the same address value." | As in comment | Revised –Agree in principle with the commenter since the AP can transmit an DL MU PPDU that contains A-MPDUs that have TAs of different members of the multiple BSSID set. In order to allow this case the proposed resolution is to explicity state that exception.TGax editor to make the changes shown in 11-18/0009r0 under all headings that include CID 12445. |

**Discussion: *None.***

* Obtaining an EDCA TXOP

Change the last paragraph and insert a new paragraph as follows:

A STA shall save the TXOP holder address for the BSS in which it is associated, which is the MAC address from the Address 2 field of the frame that initiated a frame exchange sequence except when this is a CTS frame, in which case the TXOP holder address is the Address 1 field. If the TXOP holder address is obtained from a Control frame, a VHT STA or HE STA shall save the nonbandwidth signaling TA value obtained from the Address 2 field. If a non-VHT non-HE STA receives an RTS frame with the RA address matching the MAC address of the STA and the MAC address in the TA field in the RTS frame matches the saved TXOP holder address, then the STA shall send the CTS frame after SIFS, without regard for, and without resetting, its NAV. If a VHT STA or HE STA receives an RTS frame with the RA address matching the MAC address of the STA and the nonbandwidth signaling TA value obtained from the Address 2 field in the RTS frame matches the saved TXOP holder address, then the STA shall send the CTS frame after SIFS, without regard for, and without resetting, its NAV. When a STA receives a frame addressed to it that requires an immediate response, except for RTS and Trigger frames (see 27.5.3.5 (UL MU CS mechanism)), it shall transmit the response independent of its NAV. The saved TXOP holder address shall be cleared when the NAV is reset or when the NAV counts down to 0.

**TGax Editor: *Change the paragraph below of this subclause as follows (#CID 11154, 12445):***

During an EDCA TXOP, the(#6527) Address 2 field excluding the Individual/Group bit of all Control frames carried in a PPDU that is not an HE MU PPDU sent by an HE TXOP holder shall be set to the same address value. *(#11154, 12445)*