IEEE P802.11  
Wireless LANs

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| Comment Resolution on EDMG A-PPDU Structure | | | | |
| Date: 2017-5-10 | | | | |
| Author(s): | | | | |
| Name | Affiliation | Address | Phone | Email |
| Takenori Sakamoto | Panasonic | 600 Saedo-cho, Tsuzuki-ku, Yokohama 224-8539, Japan |  | [sakamoto.takenori@jp.panasonic.com](mailto:sakamoto.takenori@jp.panasonic.com) |
| Hiroyuki Motozuka | Panasonic |  |  |  |
| Gaius Wee | Panasonic |  |  |  |
| Lei Huang | Panasonic |  |  |  |

Abstract

This document proposes resolution on CID 9.

This document proposes changes on 30.5.6.2 Symbol blocking and guard insertion in Draft D0.3 [1] to specify SU PPDU structure for EDMG A-PPDU.

**Introduction**

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| --- | --- | --- | --- | --- | --- |
| CID | Page | Line | Comment | Proposed Change | Resolution |
| 9 | 100 | 16 | A-PPDU structures are missing detailed specification that can result in potential ambiguity in interpreting the standard | Add A-PPDU structure details including the GIs, headers, etc like is provides for the single PPDU case in section 30.5.6.2.2 | Accepted  PPDU structures for EDMG A-PPDU are provided in new section 30.5.6.2.4.  TGay editor to make the changes shown in 11-17/0761r3. |

**Propose**

**30.5.6.2 Symbol blocking and guard insertion**

**30.5.6.2.1 General**

*Modify the paragraph as follows (CID #9).*

GIs defined in 30.5.6.1 are inserted in the Data field of an EDMG PPDU. This subclause describes the structure of each type of EDMG PPDU and where GIs are inserted. In this subclause, the following apply:

* All references to *NCB* refer to the integer number of 2.16 GHz channels over which an EDMG PPDU is transmitted, where 1 ≤ *NCB* ≤ 4.
* All references to *i* refer to the integer number of spatial streams an EDMG PPDU is transmitted on, where 1 ≤ *i* ≤ 8.
*  is defined in 30.3.3.2.3.
* All references to *j* refer to the index number of the *j*th EDMG PPDU aggregated into an EDMG A-PPDU, where 0 ≤ *j* ≤ N.

**30.5.6.2.2 SU PPDU structure for SISO transmissions**

**30.5.6.2.3 SU PPDU structure for MIMO transmissions**

*Insert a new subclause after 30.5.6.2.3 (CID #9).*

**30.5.6.2.4 SU PPDU structure for EDMG A-PPDU**

The SU PPDU structures described in this subclause cover all the combination of channel bandwidth and number of spatial streams.

The SU PPDU structures for the first EDMG PPDU (i.e., *j* = 0) within the EDMG A-PPDU are the same as shown in Figure 84 to Figure 95. The SU PPDU structure for the EDMG PPDUs following the first EDMG PPDU (i.e., 1 ≤ *j* ≤ N) when using the short GI, normal GI and long GI shall be as shown in Figure 96, Figure 97, and Figure 98, respectively. The final block transmitted of each EDMG PPDU within the EDMG A-PPDU is followed by the same GI as the data field regardless of the value of the Additional EDMG PPDU field within the corresponding EDMG-Header-A.



1. — SU PPDU structure: EDMG A-PPDU, 1 ≤ *j* ≤ N, short GI



1. — SU PPDU structure: EDMG A-PPDU, 1 ≤ *j* ≤ N, normal GI



1. — SU PPDU structure: EDMG A-PPDU, 1 ≤ *j* ≤ N, long GI

**References:**

1. Draft P802.11ay\_D0.3

**Straw Poll/Motion:**

* Do you agree to accept resolution to CID 9 in doc 11-17/0761r3?