IEEE P802.11  
Wireless LANs

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| Base MCS and Length Calculation for Extended MCS Set | | | | |
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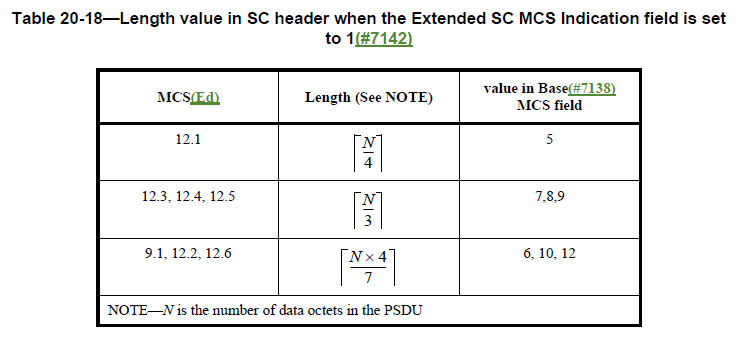
Abstract

This submission proposes to modify the base MCS and Length value in SC header for Extended SC MCS to eliminate the duration calculation error by STAs without Extended SC MCS support.

The changes are relative to Draft P802.11REVmc\_D5.4. This contribution is provided as resolution to CID 7142.

**Discussion**:

The current text in draft 5.4 of REVmc defines the Length field in the PHY SC header to be calculated based on the ratio of modulation and coding rate between Base MCS and actual extended MCS. The formulae are defined in Table 20-18 of Draft 5.4.



The formulae set the Length field value so that a packet with Base MCS and the Length field value has the same PPDU duration as a packet with the actual extended MCS and number of octets equal to *N*. But the formulae will cause a duration error of up to 582 ns for some *N*s (number of octets) . This is because the formulae didn’t consider the LDPC code word construction and symbol block construction, i.e., the number of parity bits and padding bits are not derived by the ratio of modulation and coding rate only.

The proposed formulae calculate the Length value based on the number of symbol blocks (NBLKS defined in 20.6.3.2.3.3(LDPC encoding process)) instead of ratio of modulation and coding rate, thus eliminates duration error. Also, the Base MCSs corresponding to extended MCSs are assigned in natural order.

*Proposed changes:*

*All changes are in reference to D5.4*

***Editor: modify the Description of the Length field in table 20-17 (DMG SC mode header fields) (P2513L49) as follows:***

If the Extended SC MCS Indication field is 0,(Ed) indicates the number of data octets in the PSDU; range 1–262 143.

If the Extended SC MCS Indication field is 1, the Length field value is set to , where *N* is the number of data octets in the PSDU, and *Base\_Length1* and *Base\_Length2* are computed according to Table 20-18 (Parameters for computing Length field value in SC header when the Extended SC MCS Indication field is set to 1(#7142)). The number of data octets in the PSDU shall not exceed 262 143.(#7142)

***Editor: replace the header of table 20-18 (P2515L20) as follows:***

Parameters for computing the Length field value in SC header when the Extended SC MCS Indication field is set to 1(#7142)

***Editor: replace table 20-18 with the following table:***

|  |  |  |
| --- | --- | --- |
| MCS | *Base\_Length1* (see NOTE 1 and 2) | *Base\_Length2* (see NOTE 1 and 3) |
| 9.1 |  |  |
| 12.1 |  |  |
| 12.2 |  |  |
| 12.3 |  |  |
| 12.4 |  |  |
| 12.5 |  |  |
| 12.6 |  |  |
| NOTE 1— *NBLKS* is the number of symbol blocks defined in 20.6.3.2.3.3(LDPC encoding process).  NOTE 2—*Base\_Length1* is the maximum Length value such that the packet with the base MCS specified in SC header has the given *NBLKS*.  NOTE 3— *Base\_Length2* is the maximum number of data octets in PSDU such that the packet with the extended MCS has the given *NBLKS*. | | |

***Editor: Remove the text in P2515L41-44.:***

***Editor: Modify the text in P2515L53-57 as follows:***

When the MCS belongs to the set {9.1, 12.1, 12.2, 12.3, 12.4, 12.5, 12.6}bits (X7,X6) of the initial scrambler state are set to,

where *N* is the number of data octets in the PSDU, and *Base\_Length2* is computed according to Table 20-18 (Parameters for computing Length value in SC header when the Extended SC MCS Indication field is set to 1(#7142)). Bits (X1–X5) are selected in a pseudorandom fashion making sure that at least one bit in X1-X7 is non-zero.

***Editor: Remove Table 20-19 (P2516L1)***

***Editor: modify the value of the Base MCS field in table 20-20 (DMG SC mode modulation and coding schemes(#7138)) (P2516L36) as follows:***

| **MCS** | **Base MCS Field** | **Extended SC MCS Indication Field** | **Modulation** | **NCBPS** | **Repetition** | **Code Rate** | **Data Rate**  **(Mb/s)** |
| --- | --- | --- | --- | --- | --- | --- | --- |
| 1 | 1 | 0 | π/2-BPSK | 1 | 2 | 1/2 | 385 |
| 2 | 2 | 0 | π/2-BPSK | 1 | 1 | 1/2 | 770 |
| 3 | 3 | 0 | π/2-BPSK | 1 | 1 | 5/8 | 962.5 |
| 4 | 4 | 0 | π/2-BPSK | 1 | 1 | 3/4 | 1155 |
| 5 | 5 | 0 | π/2-BPSK | 1 | 1 | 13/16 | 1251.25 |
| 6 | 6 | 0 | π/2-QPSK | 2 | 1 | 1/2 | 1540 |
| 7 | 7 | 0 | π/2-QPSK | 2 | 1 | 5/8 | 1925 |
| 8 | 8 | 0 | π/2-QPSK | 2 | 1 | 3/4 | 2310 |
| 9 | 9 | 0 | π/2-QPSK | 2 | 1 | 13/16 | 2502.5 |
| 9.1 | 6 | 1 | π/2-QPSK | 2 | 1 | 7/8 | 2695 |
| 10 | 10 | 0 | π/2-16QAM | 4 | 1 | 1/2 | 3080 |
| 11 | 11 | 0 | π/2-16QAM | 4 | 1 | 5/8 | 3850 |
| 12 | 12 | 0 | π/2-16QAM | 4 | 1 | 3/4 | 4620 |
| 12.1 | 7 | 1 | π/2-16QAM | 4 | 1 | 13/16 | 5005 |
| 12.2 | 8 | 1 | π/2-16QAM | 4 | 1 | 7/8 | 5390 |
| 12.3 | 9 | 1 | π/2-64QAM | 6 | 1 | 5/8 | 5775 |
| 12.4 | 10 | 1 | π/2-64QAM | 6 | 1 | 3/4 | 6390 |
| 12.5 | 11 | 1 | π/2-64QAM | 6 | 1 | 13/16 | 7507.5 |
| 12.6 | 12 | 1 | π/2-64QAM | 6 | 1 | 7/8 | 8085 |

**References:**

[1] Draft P802.11REVmc\_D5.4

[2] 11-16-0220-03-000m-clause-20-SC-extended-MCSs

[3] 11-16-0541-03-000m-SC-64-QAM-response-to-editor-comments