IEEE P802.11  
Wireless LANs

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| Proposed text to TGax Simulation Scenarios PSM/PSP test | | | | |
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# Summary:

The following changes are suggested for the MAC simulator section of the simulation scenario document [1]:

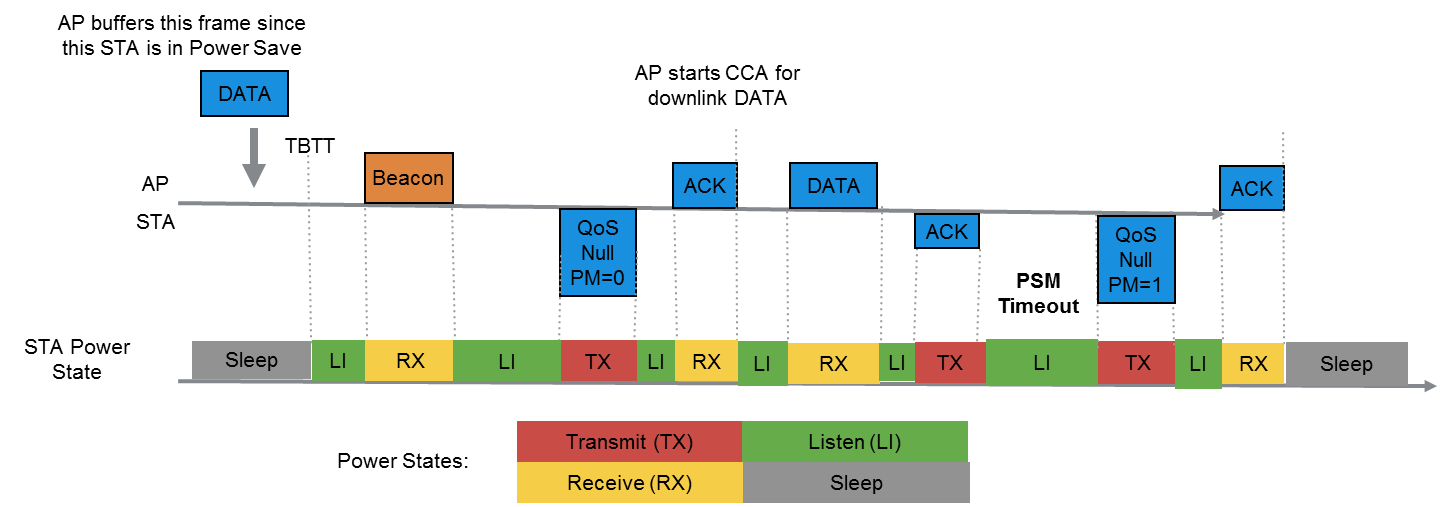
1. MAC overhead of Beacon frame is 272 octet(including TIM:250 octet)
2. Following event sequence is assumed:

* The first DL DATA arrives at 100us
* The first DTIM Beacon is sent at 100ms

1. When PS STA detects that the bit corresponding to its AID is 0 in DTIM Beacon frame, STA enters the sleep state and STA remains the sleep state until STA enters the Awake state to receive the next DTIM Beacon frame.

# Proposed text changes in <Test 5: Power Save Mechanism Test>

**PSM test:**



**Figure 11 – Example of the frameflow in PSM scenario and non-AP STA Power States.** •MSDU length: 1500 bytes with CWmin=15 downlink every 200 ms

•RTS/CTS [ OFF ]

•AIFS=DIFS=34us

•MCS = [ 0 ]

•No A-MPDU aggregation

•DTIM = [ 3 ]

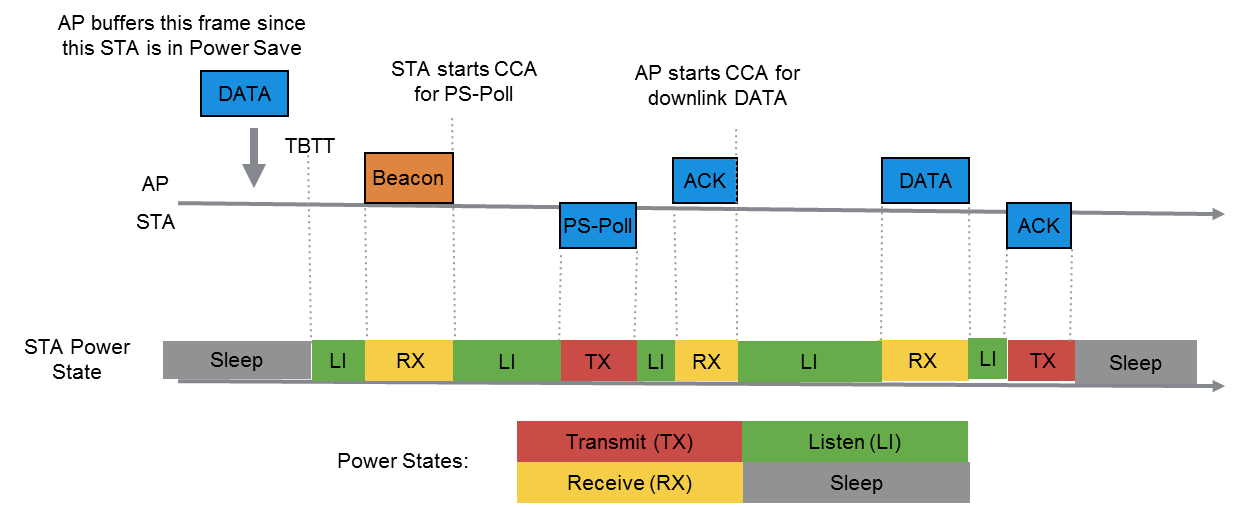
• MAC overhead of Beacon frame=272 byte•PSM timeout = [ 100 ] ms

• Following event sequence is assumed:

* The first DL DATA arrives at 100us
* The first DTIM Beacon is sent at 100ms

When PS STA detects that the bit corresponding to its AID is 0 in DTIM Beacon frame, STA enters the sleep state and STA remains the sleep state until STA enters the Awake state to receive the next DTIM Beacon frame.

**PSP test:**



**Figure 12 – Example of the frameflow in PSP scenario and non-AP STA Power States.**

•MSDU length: 1500 bytes with CWmin=15 downlink every 200 ms

•RTS/CTS [ OFF ]

•AIFS=DIFS=34us

•MCS = [ 0 ]

•No A-MPDU aggregation

•DTIM = [ 3 ]

• MAC overhead of Beacon frame=272 byte

• Following event sequence is assumed:

* The first DL DATA arrives at 100us

The first DTIM Beacon is sent at 100ms

When PS STA detects that the bit corresponding to its AID is 0 in DTIM Beacon frame, STA enters the sleep state and STA remains the sleep state until STA enters the Awake state to receive the next DTIM Beacon frame.

# References:

[1] 11-14/0980r06, “TGax Simulation Scenarios”, Simone Merlin (Qualcomm)