IEEE P802.11  
Wireless LANs

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Clause 22.3.20 Comment Resolution for D2.0, CID 4108 | | | | |
| Date: 2012-03-12 | | | | |
| Author(s): | | | | |
| Name | Affiliation | Address | Phone | email |
| Adrian Stephens | Intel Corporation |  |  | adrian.p.stephens@intel.com |
|  |  |  |  |  |

Abstract

This document provides resolutions for CIDs: 4108

| **CID** | **Page** | **Clause** | **Comment** | **Proposed Change** | **Resolution** | **Owning Ad-hoc** |
| --- | --- | --- | --- | --- | --- | --- |
| 4108 | 262.03 | 22.3.20 | The transmit flow in Figure 2-26 doesn't really work. We have a Tx PSDU Octet and and Tx Symbol box that, as shown, must be executed the same number of times. Unless we have one octet per symbol this is not so.If we have an integer number of octets per symbol, we can repeat the Tx PSDU Octet. But we don't.Instead, there must be some notion of buffering with two processes, one that fills the buffer, and one that empties it. | Rework diagram. Add notion of a buffer that holds at least a symbols worth of data. Firstly fill the buffer in one loop of Tx PSDU Octets. Then empty it in Tx Symbol.The fill buffer process can also handle the PHY padding, which is needed to provide exactly a symbol's worth of data. |  | PHY |

Proposed resolution:

Revised. Replace Figure 20-26 with the “Proposed changed figure” from document <this-number>r<last-reviewed version>

Context: Figure 20-26 from Eldad Perhia’s 11-12/0251r1.  


**Proposed changed figure:**

