

1 Technical Descriptions for
2 **Cut-Through Forwarding in Bridges**

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4 Author: Johannes Specht

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116

Part I.

117

Introduction

118 1. Purpose

119 This document is an individual contribution by the author, provided for technical
120 discussion in pre-PAR activities of IEEE 802 (i.e., Nendica). The contents of this
121 document are technical descriptions for the operations of Cut-Through Forwarding
122 (CTF) in bridges. The intent is to provide more technical clarity, and thereby also
123 address the desire expressed by some individuals during the IEEE 802 Plenary Meeting
124 in July 2022 to a certain extent.

125 2. Relationship to IEEE Standards

126 This document **IS NOT** an IEEE Standard or an IEEE Standards draft. This allows
127 readers to focus on the technical contents in this document, rather than additional
128 aspects that are important during standards development. For example:

- 129 1. The structure of this document does not comply with the structural requirements
130 for such standards. For example, it does not contain mandatory clauses for IEEE
131 Standards [1].
- 132 2. Usage of normative keywords has no implied semantics beyond explicit descrip-
133 tion. For example, usage of the words *shall*, *should* or *may* **DOES NOT** imply
134 requirements or recommendations for conformance of an implementation.
- 135 3. This document contains references, but without distinguishing between norma-
136 tive and informative references.
- 137 4. This document does not contain suggestions for assigning particular contents
138 to *vehicles* (e.g., IEEE 802 Working Groups, potential amendment projects for
139 existing standards, or potential new standard projects). As a consequence, the
140 clause structure of this document is intended for readability, rather than fitting
141 into the clause structure of a particular Standard (i.e., which would matter for
142 potential amendment projects).

143 3. Status of this Document

144 This document is work-in-progress in an early stage. It contains technical and editorial
145 errors, omissions and simplifications. Readers discovering such issues are encouraged
146 for making enhancement proposals, e.g. by sending such proposals to the author by
147 email (johannes.specht.standards@gmail.com).

148

Part II.

149

Cut-Through Forwarding in Bridges

150

4. Overview

The architecture of a bridge with support for CTF is shown in Figure 4.1, in symmetry with the architecture specified in IEEE Std 802.1Q [2, Figure 8-3].

This architecture comprises the following elements:

1. One or more higher layer entities above the MAC Service (MS) interface.
2. A bridge relay entity (7) that relays frames between different bridge Ports.
3. Generalized serial convergence operations (5) that translate between the Internal Sublayer Service (ISS) interface and Lower Layer Interface (LLI) per bridge Port.
4. Bridge Port transmit and receive operations (6) per Bridge port that transform and transfer service primitive invocations between the bridge relay entity, higher layer entities and the generalized serial convergence operations.

For differentiation between bridges with support for CTF and bridges without support for CTF, term *CTF bridge* is used in this document to refer to the former, whereas term *S&F bridge* is used in this document to refer to the latter. CTF bridges may or may not support Virtual Local Area Networks (VLANs), similar to S&F bridges, and therefore terms *VLAN-aware* and *VLAN-unaware* are used to distinguish between bridges with and without support for VLANs.

The operation of S&F bridges is specified in the respective IEEE 802.1 Standards such as IEEE Std 802.1Q[2]. The operation of CTF bridges is described in this document in the chapters referred to before, typically limiting on describing the differences to the operations of S&F bridges.

Excluded from this document are technical details on higher layer entities above the MAC service interface. The Bridge port transmit and receive operations of CTF bridges limits on Cut-Through Forwarding to frames passing through the bridge relay entity. For frames passed to higher layer entities, the bridge port transmit and receive operations of a CTF bridge establish a S&F behavior visible via the MAC service interface, and the protocols of higher layer entities can operate according to the behavior specified in IEEE 802.1 Standards unaltered. In other words, the MAC service interface

On the path through the bridge relay of CTF bridges, this document limits the scope on the operations specified in 802.1Q[2], 802.1AC[3] and IEEE Std 802.1CB[4].

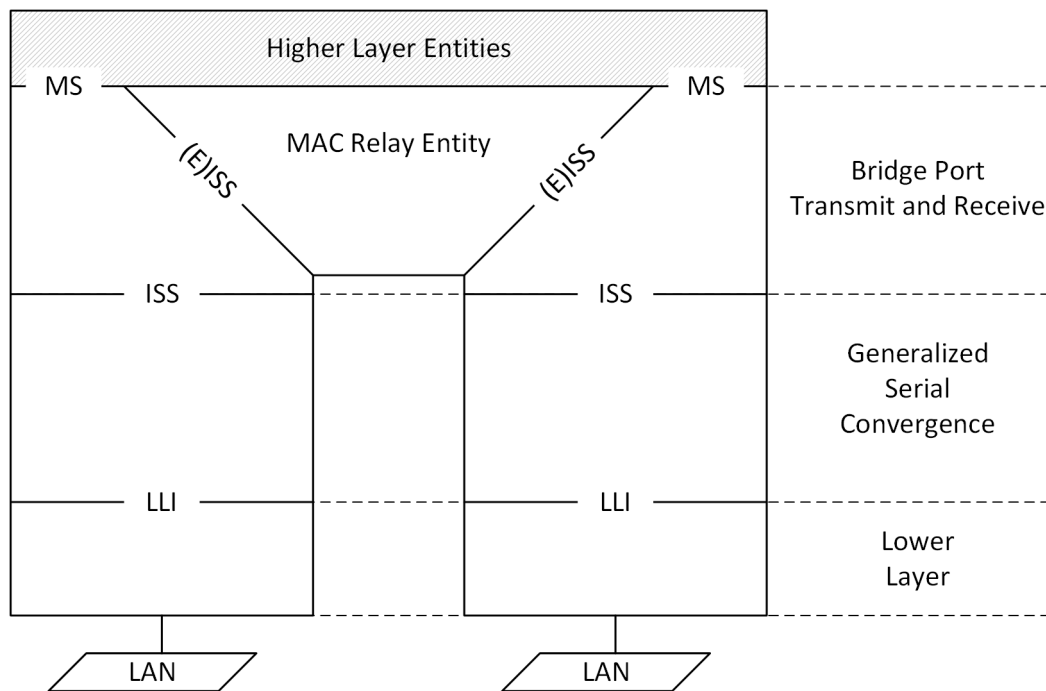
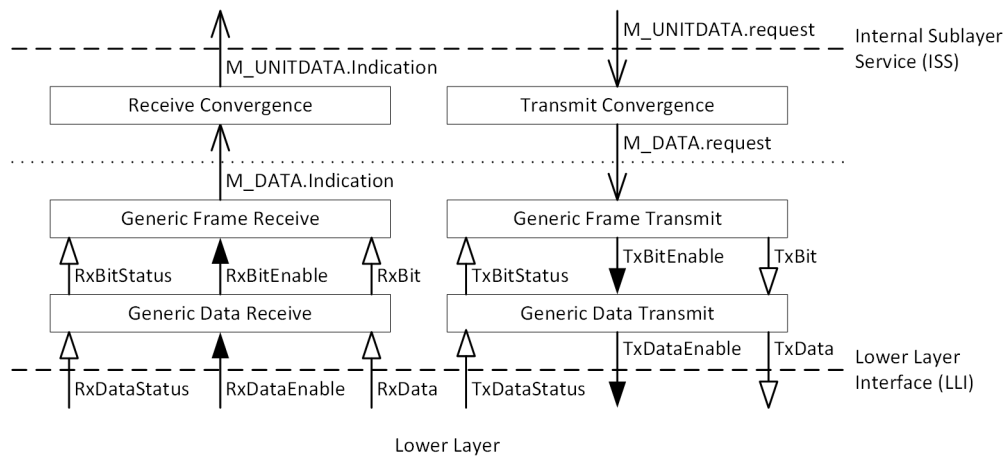


Figure 4.1.: Architecture of a Cut-Through Forwarding (CTF) Bridge.

182 **5. Generalized Serial Convergence**
 183 **Operations**

184 **5.1. Overview**

185 The generalized serial convergence operations are described by a stack of processes
 186 that interact via global variables (see 5.4) and service primitive invocations (see 5.2).
 187 These processes provide the translation between the Internal Sublayer Service (ISS)
 188 and a broad range of lower layers, including (but not limited to) physical layers. Figure
 5.1 provides an overview of these processes and their interaction¹. The processes can



NOTATION

- ▷ : A global variable set solely by the originating process.
- ▶ : A global variable set the originating process and reset by the receiving process.
- : A service primitive.

Figure 5.1.: Overview of the generalized serial convergence operations.

189 be summarized as follows:
 190

¹This interaction model is inspired by clause 6 and 8.6.9 of IEEE Std 802.1Q[2].

- 191 1. A Receive Convergence process (5.7) that translates each invocation of the M_DATA.-
 192 indication service primitive (5.2.1) into a corresponding invocation of the M_UNIT-
 193 DATA.indication service primitive (5.2.2).
- 194 2. A Generic Frame Receive process (5.6) that generates M_DATA.indication in-
 195 vocations for bit sequences originating from the Generic Data Receive process of
 196 at least LEN_MIN (5.3.5) bits.
- 197 3. A Generic Data Receive process (5.5) that translates a lower layer-dependent²
 198 serial data stream into delineated homogeneous bit sequences of variable length,
 199 each typically representing a frame.
- 200 4. A Transmit Convergence process (5.10) that translates each invocation of the
 201 M_UNITDATA.request service primitive into a corresponding invocation of the
 202 M_DATA.request service primitive.
- 203 5. A Generic Frame Transmit process (5.9) that translates M_DATA.request invo-
 204 cations into bit sequences for the Generic Data Transmit process.
- 205 6. A Generic Data Transmit process (5.8) that translates bit sequences from the
 206 Generic Frame Transmit process into a lower layer-dependent serial data stream.

207 The generalized serial convergence operations are inspired by the concepts described
 208 in slides by Roger Marks [5, slide 15], but follow a different modeling approach with
 209 more formalized description of these functions and incorporate some of the following
 210 concepts, as suggested by the author of this document during the Nendica meetings
 211 on and after August 18, 2022. The differences can be summarized as follows:

- 212 – Alignment with the state machine diagram conventions in Annex E of IEEE Std
 213 802.1Q[2].
- 214 – Support for serial data streams from lower layers with arbitrary data word
 215 length³.
- 216 – Explicit modeling of atomic ISS service primitive invocations.

217 By keeping ISS service primitive invocations atomic, the approach in this document
 218 is intended to provide a higher level of compatibility with existing IEEE 802.1 Stds,
 219 similar to the modeling approach via frame look-ahead of service primitive invocation-
 220 s/prescient functions[6, slides 7ff.].

²Such a lower layer may be an entity on the physical layer (PHY), but the generalized receive operations are not limited to this.

³This generalization is intended to allow a wide range of lower layers. In addition, the support for word sizes (e.g., 8 bits, 32 bits or 64 bits) may be close to realities found in hardware implementation. It is subject to discussion whether this and other generalizations over [5] introduced by the author are considered to be helpful.

221 **5.2. Service Primitives**

222 **5.2.1. M_DATA.indication and M_DATA.request**

223 The M_DATA.indication service primitive passes the contents of a frame from the
224 Generic Frame Receive process to the Receive Convergence process. The M_DATA.-
225 request service primitive passes the contents of a frame from the Transmit Convergence
226 process to the Generic Frame Transmit process. This parameter signatures of the
227 service primitives are as follows⁴:

228 **M_DATA.indication(DA, SA, SDU, FCS)**

229 **M_DATA.request(DA, SA, SDU, FCS)**

230 The parameters are defined as follows:

231 **5.2.1.1. DA**

232 An array of zero to LEN_ADDR (5.3.3) bits, containing the destination address of a
233 frame.

234 **5.2.1.2. SA**

235 An array of zero to LEN_ADDR (5.3.3) bits, containing the source address of a frame.

236 **5.2.1.3. SDU**

237 An array of zero or more bits, containing a service data unit of a frame. The number
238 of bits after complete reception of a frame is an integer multiple LEN_OCT (5.3.2).

239 **5.2.1.4. FCS**

240 An array of zero to LEN_FCS (5.3.4) bits, containing the frame check sequence of a
241 frame.

242 **5.2.2. M_UNITDATA.indication and M_UNITDATA.request**

243 As specified in IEEE Std 802.1AC[3, 11.1], with the parameter signatures summarized
244 as follows:

⁴The parameters in this version of this document limit to those introduced in Roger Marks' GSCF slides [5]. Future versions may introduce more flexibility (e.g., for IEEE Std 802.11 [7, 9.2]).

```

M_UNITDATA.indication(
    destination_address,
    source_address,
    mac_service_data_unit,
    priority,
245    drop_eligible,
    frame_check_sequence,
    service_access_point_identifier,
    connection_identifier
)

M_UNITDATA.request(
    destination_address,
    source_address,
    mac_service_data_unit,
246    priority, drop_eligible,
    frame_check_sequence,
    service_access_point_identifier,
    connection_identifier
)

```

247 5.2.3. Atomic Invocation Models

248 5.2.3.1. Bit-Accurate Modeling

249 All invocations of service primitives in this document are atomic. That is, each in-
250 vocation is non-dividable (see also 7.2 of IEEE Std 802.1AC[3]). Service primitive
251 invocations are modeled more explicitly in this document, allowing for accurate de-
252 scription of operations within a Bridge, while retaining atomicity. This explicit model
253 comprises the following:

- 254 1. A service primitive provides two attributes⁵, *'start* and *'end*. These attributes
255 are used in subsequent descriptions to indicate the start and the end of the
256 indication, respectively.
- 257 2. The parameters of a service primitive are explicitly modeled as bit arrays.
- 258 3. The values of parameters during invocations of a service primitive are passed
259 according to a call-by-reference scheme.

260 In a series of sequential *processing stages* (e.g., the processes introduced in 5.1 or a
261 sub-process of the forwarding process in 7), this model allows later processing stages
262 to access contents in service primitive parameters that are incrementally added by an
263 earlier processing stage.

⁵The concept of *attributes* is inspired by the *Very High Speed Integrated Circuits Hardware De-
description Language*, VHDL[8], which provides predefined attributes (e.g., *'transaction*) that allow
modeling over multiple VHDL simulation cycles at the same instant of simulated time.

264 **5.2.3.2. Parameter-Accurate Modeling**

265 At higher levels processing stages, service primitives of frames and processing of these
 266 frames themselves is modeled at parameter level accuracy. The purpose of this model
 267 is to

- 268 1. provide means for compact description of temporal control (5.2.3.3) in and across
 269 processing stages,
- 270 2. enable re-use of existing transformation rules from IEEE 802.1 Stds by reference,
 271 and
- 272 3. avoid low level details that would not provide any value to the clarity and un-
 273 mambiguous descriptions.

274 The parameter-accurate operates at the resolution of symbolic and/or numeric param-
 275 eters instead of bit arrays (5.2.3.1). A parameter is said to be *complete* at the earliest
 276 instant of time at which the *minimal information* is available to *unambiguously* deter-
 277 mine the parameter's value within the specified valid value range of such parameter.
 278 The minimal information may be

- 279 1. a bit slice in the bit stream of a frame (i.e., a coherent sequence of bits in a
 280 frame's, in order),
- 281 2. the result of composition and/or computation across bits located at various lo-
 282 cations in a frame,
- 283 3. based on out-of-band information, or
- 284 4. any combination of the aforesaid.

285 As an example, the `vlan_identifier` parameter of `EM_UNITDATA.indication` (6.3)
 286 invocations can be derived from a subset of underlying bits of the associated `SDU`
 287 parameter of `M_DATA.indication` invocations (5.2.1) that are located in a VLAN Tag
 288 according to the specification of the Support for the EISS defined in IEEE Std 802.1Q
 289 [2, item e) in 6.9.1] or originate from out-of-band information like a configured per-
 290 Port PVID parameter [2, item d) in 6.9, item f) in 6.9.1 and 12.10.1.2].

291
 292 Most of the data transformations between bits in a frame, frame parameters and
 293 potential out-of-band information is already unambiguously specified in the relevant
 294 IEEE 802.1 Standards. This document omits repetition of already specified transfor-
 295 mations and instead just refers to the relevant data transformations in existing IEEE
 296 802.1 Standards.

297 **5.2.3.3. Temporal Control**

298 Parameter-accurate modeling allows formulating temporal control in processing stages.
 299 A processing stage (5.2.3.1) may *stall* further processing of a frame, including (but
 300 not limited to) passing this frame to a subsequent processing stage, until one or more

301 parameters are complete (5.2.3.2). Most processing stalls are implicit due to the data
 302 dependencies already specified in IEEE 802.1 Standards (e.g., Ingress Filtering as part
 303 of the forwarding process in IEEE Std 802.1Q[2, 8.6.2] depends on the availability of
 304 a frame's VID, which therefore implicitly requires completion of the `vlan_identifier`
 305 parameter of `EM_UNITDATA.indication` invocations), however, explicit modeling of
 306 processing stalls may be expressed by formulations in natural language.

307 Example formulations:

- 308 1. *“Processing **stalls** pending the **vlan_identifier** parameter.”*
- 309 2. *“Further execution in a MAC bridge is **stalled** pending the **destination ad-***
 310 ***dress** of a frame prior to the forwarding database lookup of the destination*
 311 *ports.”*

312 5.3. Global Constants

313 5.3.1. PREAMBLE

314 A lower layer-dependent array of zero⁶ or more bits, containing the expected preamble
 315 of each frame.

316 5.3.2. LEN_OCT

317 The integer number eight (8), indicating the number of bits per octet.

318 5.3.3. LEN_ADDR

319 An integer denoting the length of the DA and SA parameters of `M_DATA.indication`
 320 parameters, in bits. For example,

$$\text{LEN_ADDR} = 48 \tag{5.1}$$

321 indicates an EUI-48 addresses.

322 5.3.4. LEN_FCS

323 An integer denoting the length of frame check sequence and the length FCS parameter
 324 of `M_DATA.indication` parameter, respectively, in bits. For example,

$$\text{LEN_FCS} = 32 \tag{5.2}$$

325 indicates a four octet frame check sequence.

⁶Including length zero permits to support lower layers that do not expose a preamble to the Generic Data Receive process.

326 **5.3.5. LEN_MIN**

327 A lower layer-dependent integer, denoting the minimum length of a frame, in bits.
 328 Invocation of the M_DATA.indication service primitive starts once the Generic Frame
 329 Receive process received the first LEN_MIN bits of a frame. Values for LEN_MIN
 330 with

$$\text{LEN_MIN} \geq \text{PREAMBLE.length} + \text{LEN_FCS} \quad (5.3)$$

331 are valid.

332 **5.3.6. LEN_MAX**

333 A lower layer-dependent integer, denoting the maximum length of a frame, in bits. In-
 334 vocation of the M_DATA.indication service primitive ends at latest once the Generic
 335 Frame Receive process received at most LEN_MAX bits of a frame. Values for
 336 LEN_MIN with

$$\text{LEN_MAX} \geq \text{PREAMBLE.length} + 2\text{LEN_ADDR} + \text{LEN_FCS} \quad (5.4)$$

337 are valid.

338 **5.3.7. LEN_DATA**

339 A lower layer-dependent integer, denoting the width of the RxData variable, in bits.

340 **5.4. Global Variables**

341 **5.4.1. RxBitEnable**

342 A Boolean variable, set by the Generic Data Receive process and reset by the Generic
 343 Frame Receive process, which indicates an update of the RxBit variable, RxBitStatus
 344 variable, or both.

345 **5.4.2. RxBit**

346 A bit variable used to pass a single bit value to the Generic Frame Receive process.

347 **5.4.3. RxBitStatus**

348 An enumeration variable used to pass the receive status from the Generic Data Receive
 349 process to the Generic Frame Receive process. The valid enumeration literals are as
 350 follows:

351 **RECEIVING** Indicates that the Generic Data Receive process received data from lower
 352 layers in a serial stream without knowledge of the remaining length of the overall
 353 data stream.

Algorithm 5.1 Definition of data type `low_data_t`.

```
typedef struct {
    Boolean start;
    Boolean end;
    bit [] value;
} low_data_t;
```

354 **TRAILER** Indicates that the Generic Data Receive process received data from lower
 355 layers in a serial stream with the knowledge that `LEN_FCS` or less bits follow.

356 **5.4.4. RxDataEnable**

357 A Boolean variable, set by a lower layer and reset by the Generic Data Receive process,
 358 which indicates an update of the `RxData` variable, `RxDataStatus` variable, or both.

359 **5.4.5. RxData**

360 A variable of composite data type `low_data_t`, used for serially passing data words of
 361 frames from a lower layer to the Generic Data Receive process. Type `low_data_t` is
 362 defined in Listing 5.1. The semantics of the constituent parameters is as follows:

363 **start** Indicates whether the data word is the first word of a frame (TRUE) or not
 364 (FALSE).

365 **end** Indicates whether the data word is the last word of a frame (TRUE) or not
 366 (FALSE).

367 **value** A lower layer-dependent non-empty array of up to `LEN_DATA` (5.3.7) bits,
 368 containing a data word of a frame. An array length less than `LEN_DATA` bits
 369 is only valid if `end` is TRUE.

370 **5.4.6. RxDataStatus**

371 An enumeration variable used to pass the receive status from lower layers to the Generic
 372 Data Receive process. The valid enumeration literals are as follows:

373 **RECEIVING** Indicates that data stream reception from lower layers is active.

374 **IDLE** Indicates that data stream reception from lower layers is not active.

375 **5.4.7. TxBitEnable**

376 A Boolean variable, set by the Generic Frame Transmit process and reset by the
 377 Generic Data Transmit process, which indicates an update of the `TxBit` variable.

378 **5.4.8. TxBit**

379 A bit variable used to pass a single bit value to the Generic Data Transmit process.

380 **5.4.9. TxBitStatus**

381 An enumeration variable that establishes a back pressure mechanism from the Generic
382 Data Transmit process to the Generic Frame Transmit process. The valid enumeration
383 literals are as follows:

384 **READY** Indicates that the Generic Data Transmit process can accept one or more
385 bit(s) from the Generic Frame Transmit process.

386 **BUSY** Indicates that the Generic Data Transmit process cannot accept bits from the
387 Generic Frame Transmit process.

388 **5.4.10. TxDataEnable**

389 A Boolean variable, set by the Generic Data Transmit process a lower layer and reset
390 by the lower layer, which indicates an update of the TxData variable.

391 **5.4.11. TxData**

392 A variable of composite datatype `low_data_t` (5.1), used for serially passing data
393 words of frames from the Generic Data Transmit process to a lower layer.

394 **5.4.12. TxDataStatus**

395 An enumeration variable that establishes a back pressure mechanism from the lower
396 layer to the Generic Data Transmit process. The valid enumeration literals are as
397 follows:

398 **READY** Indicates that a lower layer can accept one or more bit(s) from the Generic
399 Data Transmit process.

400 **BUSY** Indicates that a lower layer cannot accept bits from the Generic Data Transmit
401 process.

402 **5.5. Generic Data Receive**

403 The Generic Data Receive process translates a lower layer-dependent⁷ serial data
404 stream into a uniform bit stream. In addition, it realizes the following functions:

- 405 – Determine the position in the serial data stream of a frame at which the frame
406 check sequence begins (delay line modeling).

⁷Such a lower layer may be an entity on the physical layer (PHY), but the generalized receive operations are not limited to this.

- 407 – Truncate excess bits to satisfy the frame length requirements implied by the
408 parameter definition of the M_DATA.indication primitive (5.2.1).

409 **5.6. Generic Frame Receive**

410 **5.6.1. Description**

411 The Generic Frame Receive process transforms a serial bit streams of frames from the
412 Generic Data Receive process into invocations of the M_DATA.indication primitive.

413 **5.6.2. State Machine Diagram**

414 The operation of the Generic Frame Receive process is specified by the state machine
415 diagram in Figure 5.2 , using the variables and functions defined in subsequent sub-
416 clauses.

417 **5.6.3. Variables**

418 **5.6.3.1. cnt**

419 An integer counter variable, used to count the number of bits in the current parameter
420 of the frame.

421 **5.6.3.2. len**

422 An integer variable holding the actual length of a frame under reception, in bits.

423 **5.6.3.3. status**

424 An enumeration variable holding the current status of the Generic Frame Receive
425 process. The valid enumeration literals are as follows:

426 **Ok** Indicates that no error has been discovered prior or during frame reception.

427 **FrameTooLong** Indicates that a frame under reception exceeded LEN_MAX bits.

428 **FCSInvalid** Indicates inconsistency between the FCS parameter an the remaining pa-
429 rameters of a frame under reception.

430 **5.6.4. Functions**

431 **5.6.4.1. append(parameter,bit)**

432 The append function appends a given bit at the end of a particular parameter of an
433 M_DATA.indication service primitive.

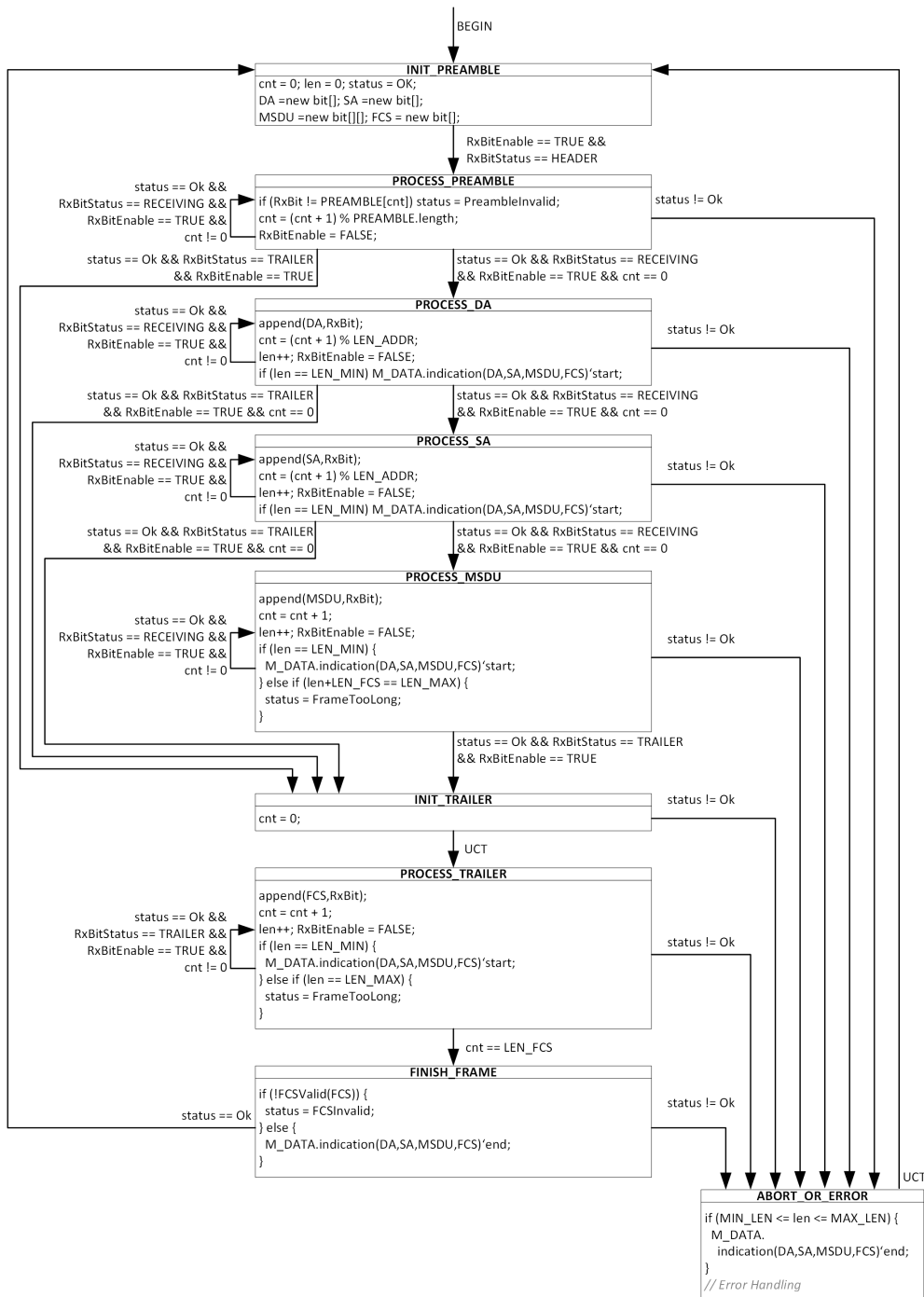


Figure 5.2.: State Machine Diagram of the Generic Frame Receive Process.

434 **5.6.4.2. FCSValid(FCS)**

435 The FCSValid function determines if the FCS parameter consistent with the remaining
436 parameters of the M_DATA.indication service primitive (TRUE) or not (FALSE).

437 **5.7. Receive Convergence**

438 The Receive Convergence Process implements the translation of M_DATA.indication
439 invocations to M_UNITDATA.indication invocations. The supported translations are
440 lower layer-dependent and include, but are not limited to, those specified in clause 13
441 of IEEE Std 802.1AC[3].

442 Each M_DATA.indication invocation results in an associated M_UNITDATA.-
443 indication invocation. During the translation, the M_UNITDATA.indication param-
444 eters are extracted from the M_DATA.indication parameters according to the rules
445 defined for the underlying lower layer.

446 **5.8. Generic Data Transmit**

447 PLACEHOLDER, for descriptions symmetrical to 5.5.

448 **5.9. Generic Frame Transmit**

449 PLACEHOLDER, for descriptions symmetrical to 5.6.

450 **5.10. Transmit Convergence**

451 PLACEHOLDER, for descriptions symmetrical to 5.7.

452 6. Bridge Port Transmit and 453 Receive Operations

454 6.1. Overview

455 The architecture of the bridge port transmit and receive operations in CTF bridges is
456 identical to the architecture of S&F bridges. The architecture is shown in Figure 6
457 and comprises the following elements:

- 458 1. Connectivity (6.2) between the access points of the generalized serial convergence
459 operations (5), higher layer entities, and the bridge relay entity (7).
- 460 2. Translations between ISS and EISS (6.3).

461 6.2. Bridge Port Connectivity

462 Bridge Port connectivity in a CTF bridge is identical to S&F bridges specified in IEEE
463 Std 802.1Q [2, 8.5.1] with the additions described in this section.

464 For frames under reception originating from the generalized serial convergence oper-
465 ations, a copy of such frames for each access point determined according to the
466 rules in 8.5.1 IEEE 802.1Q. If a frame copy is destined to the bridge relay entity and
467 the `CTFReceptionEnableParameter` (8.2.4) of the reception Port is set `TRUE`, this
468 copy is passed instantaneously to the translation from ISS to EISS (6.3). In all other
469 cases, CTF bridges fall-back to S&F for frames under reception originating from teh
470 generalized serial convergence operations prior to passing the respective copies to the
471 associated acces point.

472 Frames originating from the bridge relay entity or higher layer entities destined for
473 the generalized serial convergence operations are passed instantaneously to the latter.
474 The multiplexing rules in this case are identical to those of S&F bridges except that
475 frames under reception originating from the bridge relay entity are deemed as complete
476 frames for which no subesequent contents are expected.

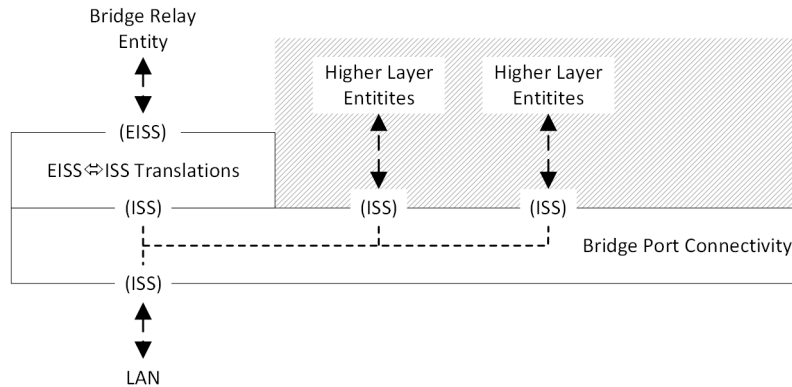


Figure 6.1.: Bridge Port Transmit and Receive.

477 **6.3. Translations between Internal Sublayer Service**
 478 **(ISS) and Enhanced Internal Sublayer Service**
 479 **(EISS)**

480 **6.3.1. Data translations**

481 Data translation from service primitive invocations of the ISS and service primitive
 482 invocations of the EISS follows the associated rules specified in IEEE Std 802.1Q [2,
 483 6.9].

484 **6.3.2. Temporal relationship**

485 **6.3.2.1. Data indications**

486 The temporal relationship (5.2.3.3) between M_UNITDATA.indication invocations of
 487 the ISS and the EM_UNITDATA.indication invocations of the EISS is as follows:

- 488 1. For EM_UNITDATA.indication invocations, EM_UNITDATA.indication's start
 489 and EM_UNITDATA.indication's end follow instantaneously after M_UNITDATA.-
 490 indication's start and M_UNITDATA.indication's end, respectively.

491 **6.3.2.2. Data requests**

492 The temporal relationship between EM_UNITDATA.request invocations of the EISS
 493 and the EM_UNITDATA.request invocations of the ISS is as follows:

- 494 1. For EM_UNITDATA.request invocations, M_UNITDATA.request's start and M_UNIT-
 495 DATA.request's end follow instantaneously after EM_UNITDATA.indication's start
 496 and EM_UNITDATA.indication's end, respectively.

497 7. Bridge Relay Operations

498 7.1. Overview

499 The forwarding process of a CTF bridge is shown in Figure 7.1 , and comprises the
500 processing stages of a CTF bridge [2, 8.6] with support for FRER [4, 8.1]:

- 501 1. Active topology enforcement (7.2)
- 502 2. Ingress filtering (7.3)
- 503 3. Frame filtering (7.4)
- 504 4. Egress filtering (7.5)
- 505 5. Flow classification and metering (7.6)
- 506 6. Individual recovery (7.7)
- 507 7. Sequence recovery (7.8)
- 508 8. Sequence encode (7.9)
- 509 9. Active stream identification (7.10)
- 510 10. Queuing frames (7.11)
- 511 11. Transmission selection (7.13)

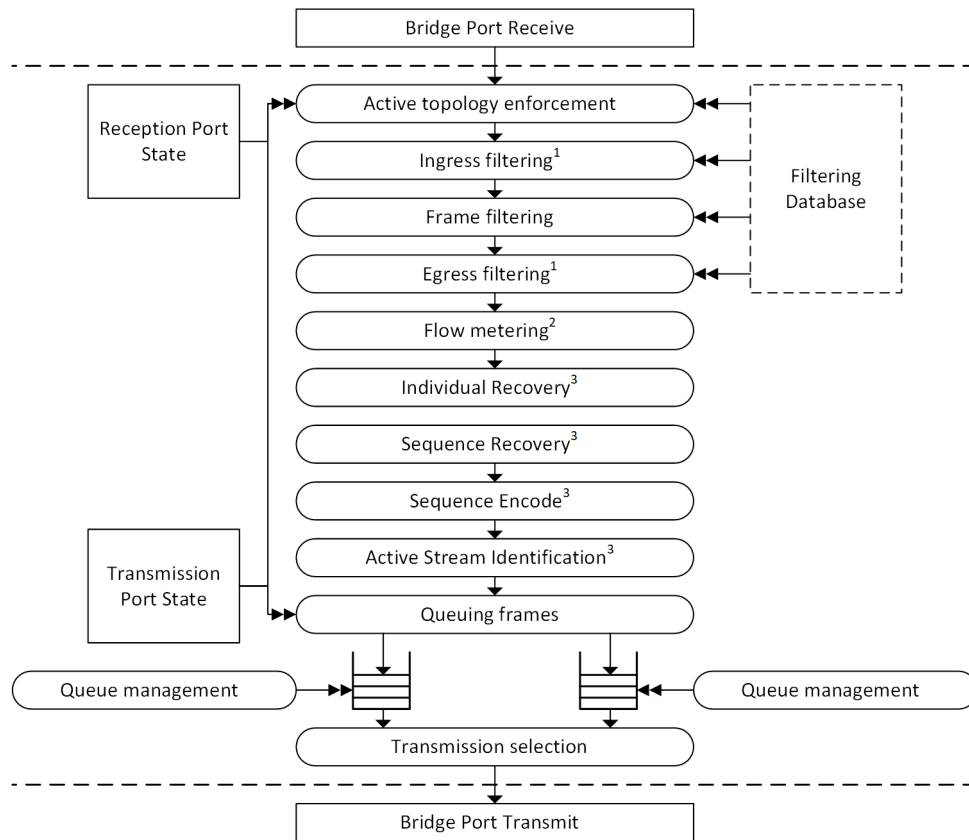
512 7.2. Active Topology Enforcement

513 7.2.1. Overview

514 The active topology enforcement operations in a CTF bridge are as specified in IEEE
515 Std 802.1Q [2, 8.6.1], with the differences described in the following for learning and
516 forwarding separately.

517 7.2.2. Learning

518 Learning determines the source address and VID parameters for subsequent submission
519 to the filtering database (FDB) according to the rules as specified in IEEE Std 802.1Q.
520 The difference in CTF bridges are that



Notes

- 1: Optional - present in VLAN-aware CTF Bridges (absent in VLAN-unaware CTF Bridges).
- 2: Optional - present if PSFP is supported.
- 3: Optional - present if FRER is supported.

Figure 7.1.: Forwarding process of a CTF bridge.

- 521 – submission of source address and VID parameters does not take place prior to
- 522 the entire associated associated frames, including FCS, are available, and
- 523 – only for frames with a consistent FCS.

524 7.2.3. Forwarding

525 Forwarding determines the set of potential transmission Ports according to the rules
 526 and controls specified in IEEE Std 802.1Q. If identification of this set depends on
 527 parameters of a frame such as VID or source address, processing is stalled pending all
 528 necessary parameters prior to passing the frame to the next processing stage, which
 529 is ingress filtering (7.3) for VLAN-aware CTF bridges and frame filtering (7.4) for
 530 VLAN-unaware CTF bridges. In absence of such dependencies, the frame is passed to
 531 the next processing stage instantaneously.

532 7.3. Ingress Filtering

533 Similar to IEEE Std 802.1Q [2, 8.6.2], ingress filtering operations are only available in
 534 VLAN-aware CTF bridges, and may discard frames received on Ports that are not in
 535 the member set [2, 8.8.10] associated with the VID parameter of these frames. The
 536 rules and controls for these operations are identical to those specified in IEEE Std
 537 802.1Q. Frames are stalled by VLAN-aware CTF bridges pending the VID parameter
 538 and passed to the next processing stage (7.4) unless they are not in the member set
 539 and therefore not passed but instantaneously discarded instead.

540 7.4. Frame Filtering

541 Frame filtering operations in CTF bridges are as specified in IEEE Std 802.1Q [2,
 542 8.6.1], with the differences and additions described in the following.

543 Similar to IEEE Std 802.1Q, frame filtering in CTF bridges can reduce the set of
 544 potential transmission Ports of each received frame on the basis of the rules and per-
 545 frame parameters specified in IEEE Std 802.1Q (destination address, VID, etc.). The
 546 exact set of parameters for each frames is determined identical to the rules specified in
 547 IEEE Std 802.1Q. If necessary, a CTF bridge stalls processing pending all necessary
 548 per-frame parameters before performing an FDB query for a frame [2, 8.8.9]. Depen-
 549 dent on the query’s evaluation by the FDB, the frame is either passed to the next
 550 processing stage instantaneously, or processing of this frame falls back to S&F:

- 551 – Whenever the query evaluation results in flooding (i.e., query evaluation hits an
- 552 “ELSE Forward” branch in 8.8.9 of IEEE Std 802.1Q), processing of the frame
- 553 falls back to S&F prior to passing it to the next processing stage¹.
- 554 – In all other cases, the frame is passed to the next processing stage instanta-
- 555 neously.

¹This fallback eliminates several cases for circulation of inconsistent frames in topological loops.

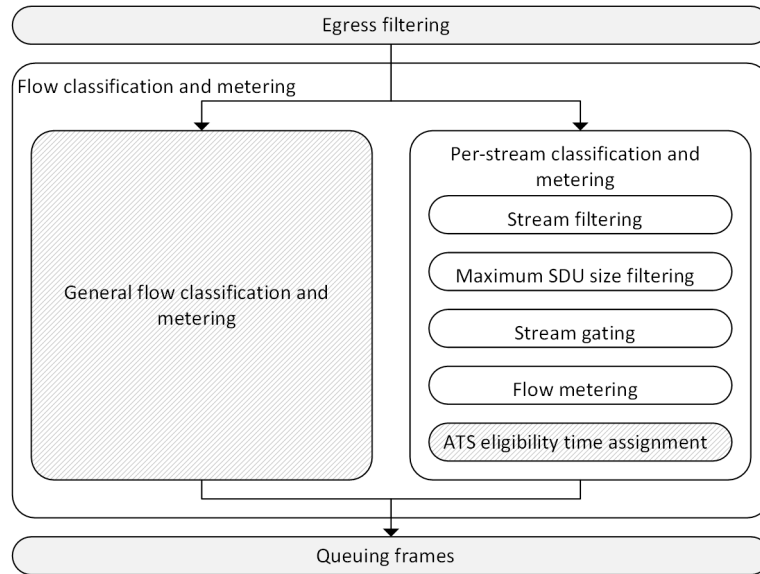


Figure 7.2.: Flow classification and metering.

556 7.5. Egress Filtering

557 Similar to IEEE Std 802.1Q [2, 8.6.2], egress filtering operations are only available
 558 in VLAN-aware CTF bridges, and reduces the set of potential transmission Ports of
 559 a frame by removing each Port that is not in the member set [2, 8.8.10] based on
 560 the frame's VID parameter. Frames are passed to the next processing stage instantana-
 561 nously after completion of these operations².

562 7.6. Flow Classification and Metering

563 7.6.1. General

564 Flow classification and metering in CTF bridges comprises the same elements and
 565 processing stages as specified in IEEE Std 802.1Q [2, 8.6.5]. These elements and
 566 relationships are show in Figure 7.2 .

567 CTF bridges fall back to S&F for frames under reception that is subject to processing
 568 by elements and stages of flow classification and metering prior to being processed by
 569 such elements and stages, unless the elements and stages that process a frame under
 570 reception limits to the following ones:

- 571 1. Stream filtering

²It is not required to stall processing pending the a frame's VID, because this already happened during ingress filtering (7.3).

572 2. Maximum SDU size filtering

573 3. Stream gating

574 4. Flow metering

575 The operation of these stages for frames under reception is described in 7.6.2, 7.6.3,
576 7.6.4 and 7.6.5. With the exception of stream filtering, all aforesaid stages process
577 frames under reception instantaneously (i.e., stall-free operation). When one of these
578 stages passed a frame under reception to a subsequent processing stage, the associated
579 frame counters of the stream filtering [2, items h) through m) in 8.6.5.3] are increased
580 according to the rules specified in IEEE 802.1Q.

581 7.6.2. Stream Filtering

582 Frames under reception are associated with stream filters according to the same
583 rules as specified in IEEE Std 802.1Q [2, 8.6.5.3]. If this association depends on a
584 *stream_handle* parameter specified in IEEE Std 802.1CB [4], processing is stalled
585 pending on this parameter prior to associating a stream filter. An associated stream
586 filter then performs all necessary associations with streams gates, maximum SDU size
587 filtering and flow metering, and passes frames to the associated stages instantaneously.

588 7.6.3. Maximum SDU size filtering

589 The operation of maximum SDU size filtering for frames under reception is as specified
590 in IEEE Std 802.1Q [2, 8.6.5.3.1] with the additions in this section. When a frame
591 under reception reaches maximum SDU size filtering, an initial number of octets of this
592 frame is already received. This number of octets is used by maximum SDU size filtering
593 for the decision on whether or not this frame is passed to a subsequent processing stage
594 or immediately discarded. If a frame under reception already passed frame maximum
595 SDU size filtering and the associated maximum SDU size limit is exceeded prior to
596 the frame's end of reception, a late error for this frame is indicated for handling by
597 subsequent processing stages in a CTF bridge.

598 7.6.4. Stream Gating

599 The operation of stream gates for frames under reception is as specified in IEEE Std
600 802.1Q [2, 8.6.5.4] with the additions in this section. Once a frame under reception
601 reaches a stream gate, this frame is only passed to the next processing stage if the
602 gate is in an open state. The frame is discard otherwise prior to being passed to the
603 next processing stage. If a stream If a stream gate closes prior to the end of the frame
604 under reception, a late error for this frame is indicated immediately for handling by
605 subsequent processing stages in a CTF bridge.

606 **7.6.5. Flow Metering**

607 The operation of stream gates for frames under reception is as specified in IEEE Std
608 802.1Q [2, 8.6.5.5] with the additions in this section. When a frame under reception
609 reaches flow metering, an initial number of octets of this frame is already received.
610 This number of octets is used by the associated flow meter for the decision on whether
611 or not this frame is passed to a subsequent processing stage or immediately discarded.
612 The frame is discard otherwise prior to being passed to the next processing stage. If
613 a frame under reception already passed flow metering and the limit of the flow meter
614 would be exceeded prior to the frame's end of reception, a late error for this frame is
615 indicated immediately for handling by subsequent processing stages in a CTF bridge.

616 **7.7. Individual Recovery**

617 PLACEHOLDER, for describing differences and additions to 7.5 of IEEE Std 802.1CB.

618 **7.8. Sequence Recovery**

619 PLACEHOLDER, for describing differences and additions to 7.4.2 of IEEE Std 802.1CB.

620 **7.9. Sequence Encode**

621 PLACEHOLDER, for describing differences and additions to 7.6 of IEEE Std 802.1CB.

622 **7.10. Active Stream Identification**

623 PLACEHOLDER, for describing differences and additions to 6.2 of IEEE Std 802.1CB.

624 **7.11. Queuing Frames**

625 The rules to determine transmission Ports, traffic classes of these Ports and the or-
626 dering requirements for frames under reception are identical to the ones specified in
627 IEEE Std 802.1Q [2, 8.6.6] with the additions in this section.

628 Frames under reception and frames that were fully received are queued in the of
629 arrival at the queuing frames processing stage. For each frames under reception, a per
630 Port copy of these frames for each transmission port in the remaining set of potential
631 transmission Ports is created before each individual copy is treated individually per
632 transmission Port. For each copy, a CTF bridge determines whether or not it is
633 queued by comparing the nominal transmission data rate of the transmission Port with
634 the nominal transmission rate of all potential reception Ports from which the frame
635 may originate and by evaluating the CTFTransmissionEnable management parameters
636 (8.2.2) as follows:

637 If the CTFTransmissionEnable parameter of the traffic class for a frame under re-
638 ception is FALSE, processing of this frames falls back to S&F before the frame is
639 queued.

640 If the CTFTransmissionEnable parameter of the traffic class for a frame under re-
641 ception is TRUE, the frame is queued if the nominal data rate of the transmission
642 Port is less than or equal to the nominal data rate of all potential reception Ports.

643 If the CTFTransmissionEnable parameter of the traffic class for a frame under re-
644 ception is TRUE, the frame is discarded if the nominal data rate of the transmission
645 Port greater than the nominal data rate of at least one potential reception Ports of
646 this frame.

647 7.12. Queue Management

648 The rules for removal of frames from queues are identical to the ones specified in IEEE
649 Std 802.1Q [2, 8.6.7].

650 7.13. Transmission Selection

651 Queued frames are become available for transmission according the rules and oper-
652 ations specified in IEEE Std 802.1Q [2, 8.6.8], with the following additions. Frames
653 under reception can only be selected for transmission by traffic classes that use the
654 strict priority transmission selection algorithm [2, 8.6.8.1]. For traffic classes that
655 other transmission selection, the frames become available for transmission selection no
656 earlier than the end of reception is reached³.

³This decision logic may be moved to 7.11 in the future.

657 8. Management Parameters

658 8.1. Overview

659 The management parameters for CTF fall into three categories:

- 660 1. Control Parameters (8.2)
- 661 2. Timing Parameters (8.3)
- 662 3. Error Counters (8.4)

663 The control parameters allow to (i) determine whether CTF is supported on a per Port
 664 and per Port per Traffic Class resolution, and if CTF is supported, to (ii) enable and
 665 disable CTF on these resolutions. These parameters are available in reception Ports
 666 and transmission Ports. For a pair of bridge ports, frames can only be subject to the
 667 CTF operation if CTF is supported and enabled on both Ports.

668 The timing parameters expose the delays experienced by frames passing from a
 669 particular reception Port to another transmission Port. These parameters are primarily
 670 intended for automated network and traffic configuration, for example, by a Centralized
 671 Network Controller (CNC) using the associated mechanisms from IEEE Std 802.1Q
 672 [2, clause 46].

673 The error counters expose information on frames that were subject to the CTF oper-
 674 ation in a bridge, even though such frames have consistency errors (i.e., a frame check
 675 sequence inconsistent with the remaining contents of that frame) during reception by
 676 this bridge. These counters are primarily intended for manual diagnostic purposes
 677 to support identifying erroneous links or stations, for example, by a human network
 678 administrator.

679 8.2. Control Parameters

680 8.2.1. CTFTransmissionSupported

681 A Boolean read-only parameter that indicates whether CTF on transmission is sup-
 682 ported (TRUE) or not (FALSE). There is one CTFTransmissionSupported parameter
 683 for each traffic class of each transmission Port.

684 8.2.2. CTFTransmissionEnable

685 A Boolean parameter to enable (TRUE) and disable (FALSE) CTF on transmission.
 686 There is one CTFTransmissionEnable parameter for each traffic class of each transmis-
 687 sion Port. The default value of the CTFTransmissionEnable parameter is FALSE for

688 all traffic classes of all transmission Ports. It is an error if a CTFTransmissionEnable
689 is set to TRUE if the associated CTF Transmission Supported parameter is FALSE.

690 **8.2.3. CTFReceptionSupported**

691 A Boolean read-only parameter that indicates whether CTF on reception is supported
692 (TRUE) or not (FALSE). There is one CTFReceptionSupported parameter for each
693 reception Port.

694 **8.2.4. CTFReceptionEnable**

695 A Boolean parameter to enable (TRUE) and disable (FALSE) CTF on reception.
696 There is one CTFReceptionEnable parameter for each reception Port. The default
697 value of the CTFReceptionEnable parameter is FALSE for all reception Ports. It is an
698 error if a CTFReceptionEnable is set to TRUE if the associated CTFReceptionSup-
699 ported parameter is FALSE.

700 **8.3. Timing Parameters**

701 **8.3.1. CTFDelayMin and CTFDelayMax**

702 A pair of unsigned integer read-only parameters, in units of nanoseconds, describing
703 the delay range for frames that are subject to the CTF operation and encounter zero
704 delay for transmission selection [2, 8.6.8]. This occurs when the queue for the frame's
705 traffic class is empty, the frame's traffic class has permission to transmit, and the egress
706 Port is idle (not transmitting). There is one pair of CTFDelayMin and CTFDelayMax
707 parameters per reception Port per transmission Port traffic class pair.

708 **8.4. Error Counters**

709 **8.4.1. CTFReceptionDiscoveredErrors**

710 An integer counter, counting the number of received frames with discovered consistency
711 errors. There is one CTFReceptionDiscoveredErrors parameter for each reception
712 Port. A frame with discovered consistency errors has been identified as such by a
713 bridge on the upstream path from which the frame originates and marked by that
714 an implementation-dependent marking mechanism. The value of the counter always
715 increases by one

- 716 1. if
- 717 a) the upstream bridge that applied the marking,
 - 718 b) all bridges on the path of that bridge to the reception Port associated with
 - 719 the CTFReceptionDiscoveredErrors counter and

720 c) the receiving bridge of which the reception Port is a part of are different
721 instances of the same bridge implementation, and

722 2. the underlying marking mechanism is identical for all these instances if multiple
723 marking mechanisms are supported by these instances.

724 If either of the conditions in items 1 through 2 is unsatisfied, `CTFReceptionUndiscoveredErrors`
725 may be increased instead of `CTFReceptionDiscoveredErrors`¹.

726 **8.4.2. CTFReceptionUndiscoveredErrors**

727 An integer counter, counting the number of received frames with undiscovered con-
728 sistency errors. There is one `CTFReceptionUndiscoveredErrors` parameter for each
729 reception Port. This counter is increased by one if a frame with consistency errors is
730 received at the associated reception Port and `CTFReceptionDiscoveredErrors` is not
731 increased.

¹It is assumed that there is a variety of options for implementing a frame marking mechanism. For example, by using physical layer symbols [9, 1.121 - 1.126] or special frame check sequences [10, p.54, 2.2.][11, p.17]. The current description in this document permits any marking mechanism, but the associated error counters are only consistent in networks with homogeneous implementation instances, and may be inconsistent in heterogeneous networks. However, term (`CTFReceptionDiscoveredErrors` + `CTFReceptionUndiscoveredErrors`) on a reception Port should be identical in several heterogeneous networks. A human network administrator may be able to localize erroneous links or stations solely by considering this term along multiple reception Ports across a network instead of its constituents.

732 Part III.

733 Cut-Through Forwarding in
734 Bridged Networks

735 PLACEHOLDER, for contents on using CTF in networks [10, p.46 – p.49].

736

Part IV.

737

Appendices

738 **A. Interaction of the Lower Layer**
739 **Interface (LLI) with existing**
740 **Lower Layers**

741 PLACEHOLDER, for describing the relationship Generalized Serial Convergence (5)
742 lower layer interface and existing lower layers.

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